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**An Old-Fashioned Recipe for  
Real Time**

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**Martín Abadi and Leslie Lamport**

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**October 12, 1992**

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# Systems Research Center

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Robert W. Taylor, Director

# **An Old-Fashioned Recipe for Real Time**

Martín Abadi and Leslie Lamport

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### **Authors' Abstract**

Traditional methods for specifying and reasoning about concurrent systems work for real-time systems. Using TLA (the temporal logic of actions), we illustrate how they work with the examples of a queue and of a mutual-exclusion protocol. In general, two problems must be addressed: avoiding the real-time programming version of Zeno's paradox, and coping with circularities when composing real-time assumption/guarantee specifications. Their solutions rest on properties of machine closure and realizability.

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# 1 Introduction

A new class of systems is often viewed as an opportunity to invent a new semantics. A number of years ago, the new class was distributed systems. More recently, it has been real-time systems. The proliferation of new semantics may be fun for semanticists, but developing a practical method for reasoning formally about systems is a lot of work. It would be unfortunate if every new class of systems required inventing new semantics, along with proof rules, languages, and tools.

Fortunately, no fundamental change to the old methods for specifying and reasoning about systems is needed for these new classes. It has long been known that the methods originally developed for shared-memory multiprocessing apply equally well to distributed systems [7, 11]. The first application we have seen of a clearly “off-the-shelf” method to a real-time algorithm was in 1983 [16], but there were probably earlier ones. Indeed, the “extension” of an existing temporal logic to real-time programs by Bernstein and Harter in 1981 [6] can be viewed as an application of that logic.

The old-fashioned methods handle real time by introducing a variable, which we call *now*, to represent time. This idea is so simple and obvious that it seems hardly worth writing about, except that few people appear to be aware that it works in practice. We therefore describe how to apply a conventional method to real-time systems.

Any formalism for reasoning about concurrent programs can be used to prove properties of real-time systems. However, in a conventional formalism based on a programming language, real-time assumptions are expressed by adding program operations that read and modify the variable *now*. The result can be a complicated program that is hard to understand and easy to get wrong. We take as our formalism TLA, the temporal logic of actions [13]. In TLA, programs and properties are represented as logical formulas. A real-time program can be written as the conjunction of its untimed version, expressed in a standard way as a TLA formula, and its timing assumptions, expressed in terms of a few standard parameterized formulas. This separate specification of timing properties makes real-time specifications easier to write and understand.

The method is illustrated with two examples. The first is a queue in which the sender and receiver synchronize by the use of timing assumptions instead of acknowledgements. We indicate how safety and liveness properties of the queue can be proved. The second example is an  $n$ -process mutual exclusion protocol, in which mutual exclusion depends on assumptions about the length of time taken by



the operations. Its correctness is proved by a conventional invariance argument.

We also discuss two problems that arise when time is represented as a program variable—problems that seem to have received little attention—and present new solutions. The solutions are expressed in terms of TLA, but they can be applied to any formalism whose semantics is based on sequences of states or actions.

The first problem is how to avoid the real-time programming version of Zeno’s paradox. If time becomes an ordinary program variable, then one can inadvertently write programs in which time behaves improperly. An obvious danger is deadlock, where time stops. A more insidious possibility is that time keeps advancing but is bounded, approaching closer and closer to some limit. One way to avoid such “Zeno” behaviors is to place an a priori lower bound on the duration of any action, but this can complicate the representation of some systems. We provide a more general and, we feel, a more natural solution.

The second problem is coping with the circularity that arises in open system specifications. The specification of an open system asserts that it operates correctly under some assumptions on the system’s environment. A modular specification method requires a rule asserting that, if each component satisfies its specification, then it behaves correctly in concert with other components. This rule is circular, because a component’s specification requires only that it behave correctly if its environment does, and its environment consists of all the other components. Despite its circularity, the rule is sound for specifications written in a particular style [1, 15, 17]. By examining an apparently paradoxical example, we discover how real-time specifications of open systems can be written in this style.

## 2 Closed Systems

We briefly review how to represent closed systems in TLA. A closed system is one that is self-contained and does not communicate with an environment. No one intentionally designs autistic systems; in a closed system, the environment is represented as part of the system. Open systems, in which the environment and system are separated, are discussed in Section 4.

We begin our review of TLA with an example. Section 2.2 summarizes the formal definitions. A more leisurely exposition appears in [13], and most definitions in the current paper are repeated in a list in the appendix. Section 2.3 reviews the concepts of safety [4] and machine closure [2] (also known as feasibility [5]) and relates them to TLA, and Section 2.4 defines a useful class of history variables [2].

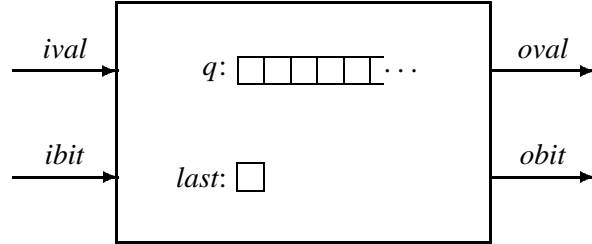


Figure 1: A simple queue.

Propositions and theorems are proved in the appendix.

## 2.1 The Lossy-Queue Example

We introduce TLA with the example of the lossy queue shown in Figure 1. The interface consists of two pairs of “wires”, each pair consisting of a *val* wire that holds a message and a boolean-valued *bit* wire. A message  $m$  is sent over a pair of wires by setting the *val* wire to  $m$  and complementing the *bit* wire. Input to the queue arrives on the wire pair  $(ival, ibit)$ , and output is sent on the wire pair  $(oval, obit)$ . There is no acknowledgment protocol, so inputs are lost if they arrive faster than the queue processes them. The property guaranteed by this lossy queue is that the sequence of output messages is a subsequence of the sequence of input messages. In Section 3.1, we add timing constraints to rule out the possibility of lost messages.

A specification is a TLA formula  $\Pi$  describing a set of allowed behaviors. A property  $P$  is also a TLA formula. The specification  $\Pi$  satisfies property  $P$  iff (if and only if) every behavior allowed by  $\Pi$  is also allowed by  $P$ —that is, if  $\Pi$  implies  $P$ . Similarly, a specification  $\Psi$  implements  $\Pi$  iff every behavior allowed by  $\Psi$  is also allowed by  $\Pi$ , so implementation means implication.

The specification of the lossy queue is a TLA formula that mentions the four variables *ibit*, *obit*, *ival*, and *oval*, as well as two internal variables:  $q$ , which equals the sequence of messages received but not yet output; and *last*, which equals the value of *ibit* for the last received message. (The variable *last* is used to prevent the same message from being received twice.) These six variables are *flexible variables*; their values can change during a behavior. We also introduce a *rigid variable*  $Msg$  denoting the set of possible messages; it has the same value

$$\begin{aligned}
Init_Q &\triangleq \wedge ibit, obit \in \{\text{true}, \text{false}\} \\
&\wedge ival, oval \in \text{Msg} \\
&\wedge last = ibit \\
&\wedge q = \langle \rangle \\
Inp &\triangleq \wedge ibit' = \neg ibit \\
&\wedge ival' \in \text{Msg} \\
&\wedge (obit, oval, q, last)' = (obit, oval, q, last) \\
EnQ &\triangleq \wedge last \neq ibit \\
&\wedge q' = q \circ \langle ival \rangle \\
&\wedge last' = ibit \\
&\wedge (ibit, obit, ival, oval)' = (ibit, obit, ival, oval) \\
DeQ &\triangleq \wedge q \neq \langle \rangle \\
&\wedge oval' = \text{Head}(q) \\
&\wedge q' = \text{Tail}(q) \\
&\wedge obit' = \neg obit \\
&\wedge (ibit, ival, last)' = (ibit, ival, last) \\
\mathcal{N}_Q &\triangleq Inp \vee EnQ \vee DeQ \\
v &\triangleq (ibit, obit, ival, oval, q, last) \\
\Pi_Q &\triangleq Init_Q \wedge \square[\mathcal{N}_Q]_v \\
\Phi_Q &\triangleq \exists q, last : \Pi_Q
\end{aligned}$$

Figure 2: The TLA specification of a lossy queue.

throughout a behavior. We usually refer to flexible variables simply as variables, and to rigid variables as *constants*.

The TLA specification is shown in Figure 2, using the following notation. A list of formulas, each prefaced by  $\wedge$ , denotes the conjunction of the formulas, and indentation is used to eliminate parentheses. The expression  $\langle \rangle$  denotes the empty sequence,  $\langle m \rangle$  denotes the singleton sequence having  $m$  as its one element,  $\circ$  denotes concatenation,  $\text{Head}(\sigma)$  denotes the first element of  $\sigma$ , and  $\text{Tail}(\sigma)$  denotes the sequence obtained by removing the first element of  $\sigma$ . The symbol  $\triangleq$  means *is defined to equal*.

The first definition is of the *predicate*  $Init_Q$ , which describes the initial state. This

predicate asserts that the values of variables *ibit* and *obit* are arbitrary booleans, the values of *ival* and *oval* are elements of **Msg**, the values of *last* and *ibit* are equal, and the value of *q* is the empty sequence.

Next is defined the *action Inp*, which describes all state changes that represent the sending of an input message. (Since this is the specification of a closed system, it includes the environment's *Inp* action.) The first conjunct,  $ibit' = \neg ibit$ , asserts that the new value of *ibit* equals the complement of its old value. The second conjunct asserts that the new value of *ival* is an element of **Msg**. The third conjunct asserts that the value of the four-tuple (*obit*, *oval*, *q*, *last*) is unchanged; it is equivalent to the assertion that the value of each of the four variables *obit*, *oval*, *q*, and *last* is unchanged. The action *Inp* is always *enabled*, meaning that, in any state, a new input message can be sent.

Action *EnQ* represents the receipt of a message by the system. The first conjunct asserts that *last* is not equal to *ibit*, so the message on the input wire has not yet been received. The second conjunct asserts that the new value of *q* equals the sequence obtained by concatenating the old value of *ival* to the end of *q*'s old value. The third conjunct asserts that the new value of *last* equals the old value of *ibit*. The final conjunct asserts that the values of *ibit*, *obit*, *ival*, and *oval* are unchanged. Action *EnQ* is enabled in a state iff the values of *last* and *ibit* in that state are unequal.

The action *DeQ* represents the operation of removing a message from the head of *q* and sending it on the output wire. It is enabled iff the value of *q* is not the empty sequence.

The action  $\mathcal{N}_Q$  is the specification's *next-state relation*. It describes all allowed changes to the queue system's variables. Since the only allowed changes are the ones described by the actions *Inp*, *EnQ*, and *DeQ*, action  $\mathcal{N}_Q$  is the disjunction of those three actions.

In TLA specifications, it is convenient to give a name to the tuple of all relevant variables. Here, we call it *v*.

Formula  $\Pi_Q$  is the internal specification of the lossy queue—the formula specifying all sequences of values that may be assumed by the queue's six variables, including the internal variables *q* and *last*. Its first conjunct asserts that  $Init_Q$  is true in the initial state. Its second conjunct,  $\square[\mathcal{N}_Q]_v$ , asserts that every step is either an  $\mathcal{N}_Q$  step (a state change allowed by  $\mathcal{N}_Q$ ) or else leaves *v* unchanged, meaning that it leaves all six variables unchanged.

Formula  $\Phi_Q$  is the actual specification, in which the internal variables *q* and *last* have been hidden. A behavior satisfies  $\Phi_Q$  iff there is some way to assign sequences

of values to  $q$  and  $last$  such that  $\Pi_Q$  is satisfied. The free variables of  $\Phi_Q$  are  $ibit$ ,  $obit$ ,  $ival$ , and  $oval$ , so  $\Phi_Q$  specifies what sequences of values these four variables can assume. All the preceding definitions just represent one possible way of structuring the definition of  $\Phi_Q$ ; there are infinitely many ways to write formulas that are equivalent to  $\Phi_Q$  and are therefore equivalent specifications.

TLA is an untyped logic; a variable may assume any value. Type correctness is expressed by the formula  $\Box T$ , where  $T$  is the predicate asserting that all relevant variables have values of the expected “types”. For the internal queue specification, the type-correctness predicate is

$$\begin{aligned} T_Q \triangleq & \wedge ibit, obit, last \in \{\text{true}, \text{false}\} \\ & \wedge ival, oval \in \text{Msg} \\ & \wedge q \in \text{Msg}^* \end{aligned} \quad (1)$$

where  $\text{Msg}^*$  is the set of finite sequences of messages. Type correctness of  $\Pi_Q$  is asserted by the formula  $\Pi_Q \Rightarrow \Box T_Q$ , which is easily proved [13]. Type correctness of  $\Phi_Q$  follows from  $\Pi_Q \Rightarrow \Box T_Q$  by the usual rules for reasoning about quantifiers.

Formulas  $\Pi_Q$  and  $\Phi_Q$  are *safety properties*, meaning that they are satisfied by an infinite behavior iff they are satisfied by every finite initial portion of the behavior. Safety properties allow behaviors in which a system performs properly for a while and then the values of all variables are frozen, never to change again. In asynchronous systems, such undesirable behaviors are ruled out by adding *fairness* properties. We could strengthen our lossy-queue specification by conjoining the *weak fairness* property  $\text{WF}_v(DeQ)$  and the *strong fairness* property  $\text{SF}_v(EnQ)$  to  $\Pi_Q$ , obtaining

$$\exists q, last : (Init_Q \wedge \Box[\mathcal{N}_Q]_v \wedge \text{WF}_v(DeQ) \wedge \text{SF}_v(EnQ)) \quad (2)$$

Property  $\text{WF}_v(DeQ)$  asserts that if action  $DeQ$  is enabled forever, then infinitely many  $DeQ$  steps must occur. This property implies that every message reaching the queue is eventually output. Property  $\text{SF}_v(EnQ)$  asserts that if action  $EnQ$  is enabled infinitely often, then infinitely many  $EnQ$  steps must occur. It implies that if infinitely many inputs are sent, then the queue must receive infinitely many of them. The formula (2) implies the *liveness property* [4] that an infinite number of inputs produces an infinite number of outputs. This formula also implies the same safety properties as  $\Phi_Q$ . A formula such as (2), which is the conjunction of an initial predicate, a term of the form  $\Box[A]_f$ , and a fairness property, is said to be in *canonical form*.

## 2.2 The Semantics of TLA

We begin with some definitions. We assume a set of constant *values*, and we let  $\llbracket F \rrbracket$  denote the semantic meaning of a formula  $F$ .

**state** A mapping from variables to values. We let  $s.x$  denote the value that state  $s$  assigns to variable  $x$ .

**state function** An expression formed from variables, constants, and operators. The meaning of a state function is a mapping from states to values. For example,  $x + 1$  is a state function such that  $\llbracket x + 1 \rrbracket(s)$  equals  $s.x + 1$ , for any state  $s$ .

**predicate** A boolean-valued state function, such as  $x > y + 1$ .

**transition function** An expression formed from variables, primed variables, constants, and operators. The meaning of a transition function is a mapping from pairs of states to values. For example,  $x + y' + 1$  is a transition function, and  $\llbracket x + y' + 1 \rrbracket(s, t)$  equals the value  $s.x + t.y + 1$ , for any pair of states  $s, t$ .

**action** A boolean-valued transition function, such as  $x > (y' + 1)$ .

**step** A pair of states  $s, t$ . It is called an  $\mathcal{A}$  *step* iff  $\llbracket \mathcal{A} \rrbracket(s, t)$  equals **true**, for an action  $\mathcal{A}$ . It is called a *stuttering* step iff  $s = t$ .

$f'$  The transition function obtained from the state function  $f$  by priming all the free variables of  $f$ , so  $\llbracket f' \rrbracket(s, t) = \llbracket f \rrbracket(t)$  for any states  $s$  and  $t$ .

$[\mathcal{A}]_f$  The action  $\mathcal{A} \vee (f' = f)$ , for any action  $\mathcal{A}$  and state function  $f$ .

$\langle \mathcal{A} \rangle_f$  The action  $\mathcal{A} \wedge (f' \neq f)$ , for any action  $\mathcal{A}$  and state function  $f$ .

*Enabled*  $\mathcal{A}$  For any action  $\mathcal{A}$ , the predicate such that  $\llbracket \text{Enabled } \mathcal{A} \rrbracket(s)$  equals  $\exists t : \llbracket \mathcal{A} \rrbracket(s, t)$ , for any state  $s$ .

Informally, we often identify a formula and its meaning. For example we say that a predicate  $P$  is true in state  $s$  instead of  $\llbracket P \rrbracket(s) = \text{true}$ .

An RTLTLA (raw TLA) formula is an expression built from actions, classical operators (boolean operators and quantification over rigid variables), and the unary temporal operator  $\square$ . The meaning of an RTLTLA formula is a boolean-valued function on

behaviors, where a behavior is an infinite sequence of states. The meaning of the operator  $\Box$  is defined by

$$\llbracket \Box F \rrbracket (s_1, s_2, s_3, \dots) \triangleq \forall n > 0 : \llbracket F \rrbracket (s_n, s_{n+1}, s_{n+2}, \dots)$$

Intuitively,  $\Box F$  asserts that  $F$  is “always” true. The meaning of an action as an RTLA formula is defined in terms of its meaning as an action by letting  $\llbracket \mathcal{A} \rrbracket (s_1, s_2, s_3, \dots)$  equal  $\llbracket \mathcal{A} \rrbracket (s_1, s_2)$ . A predicate  $P$  is an action;  $P$  is true for a behavior iff it is true for the first state of the behavior, and  $\Box P$  is true iff  $P$  is true in all states. For any action  $\mathcal{A}$  and state function  $f$ , the formula  $\Box[\mathcal{A}]_f$  is true for a behavior iff each step is an  $\mathcal{A}$  step or else leaves  $f$  unchanged. The classical operators have their usual meanings.

A TLA formula is one that can be constructed from predicates and formulas  $\Box[\mathcal{A}]_f$  using classical operators,  $\Box$ , and existential quantification over flexible variables. The semantics of actions, classical operators, and  $\Box$  are defined as before. The approximate meaning of quantification over a flexible variable is that  $\exists x : F$  is true for a behavior iff there is some sequence of values that can be assigned to  $x$  that makes  $F$  true. The precise definition appears in [13] and is recalled in the appendix. As usual, we write  $\exists x_1, \dots, x_n : F$  instead of  $\exists x_1 : \dots, \exists x_n : F$ .

A *property* is a set of behaviors that is *invariant under stuttering*, meaning that it contains a behavior  $\sigma$  iff it contains every behavior obtained from  $\sigma$  by adding and/or removing stuttering steps. The set of all behaviors satisfying a TLA formula is a property, which we often identify with the formula.

For any TLA formula  $F$ , action  $\mathcal{A}$ , and state function  $f$ :

$$\begin{aligned} \Diamond F &\triangleq \neg \Box \neg F \\ \text{WF}_f(\mathcal{A}) &\triangleq \Box \Diamond \neg(\text{Enabled } \langle \mathcal{A} \rangle_f) \vee \Box \Diamond \langle \mathcal{A} \rangle_f \\ \text{SF}_f(\mathcal{A}) &\triangleq \Diamond \Box \neg(\text{Enabled } \langle \mathcal{A} \rangle_f) \vee \Box \Diamond \langle \mathcal{A} \rangle_f \end{aligned}$$

These are TLA formulas, since  $\Diamond \langle \mathcal{A} \rangle_f$  equals  $\neg \Box [\neg \mathcal{A}]_f$ .

### 2.3 Safety and Fairness

A *finite behavior* is a finite sequence of states. We say that a finite behavior satisfies a property  $F$  iff it can be continued to an infinite behavior in  $F$ . A property  $F$  is a *safety property* [4] iff the following condition holds:  $F$  contains a behavior iff

it is satisfied by every finite prefix of the behavior.<sup>1</sup> Intuitively, a safety property asserts that something “bad” does not happen. Predicates and formulas of the form  $\Box[\mathcal{A}]_f$  are safety properties.

Safety properties form the closed sets for a topology on the set of all behaviors. Hence, if two TLA formulas  $F$  and  $G$  are safety properties, then  $F \wedge G$  is also a safety property. The *closure*  $\mathcal{C}(F)$  of a property  $F$  is the smallest safety property containing  $F$ . It can be shown that  $\mathcal{C}(F)$  is expressible in TLA, for any TLA formula  $F$ .

If  $\Pi$  is a safety property and  $L$  an arbitrary property, then the pair  $(\Pi, L)$  is *machine closed* iff every finite behavior satisfying  $\Pi$  can be extended to an infinite behavior in  $\Pi \wedge L$ . Proposition 1 below shows that machine closure generalizes the concept of fairness. The *canonical form* for a TLA formula is

$$\exists x : (Init \wedge \Box[\mathcal{N}]_v \wedge L) \quad (3)$$

where  $(Init \wedge \Box[\mathcal{N}]_v, L)$  is machine closed and  $x$  is a tuple of variables called the *internal variables* of the formula. The state function  $v$  will usually be the tuple of all variables appearing free in  $Init$ ,  $\mathcal{N}$ , and  $L$  (including the variables of  $x$ ). A behavior satisfies (3) iff there is some way of choosing values for  $x$  such that (a)  $Init$  is true in the initial state, (b) every step is either an  $\mathcal{N}$  step or leaves all the variables in  $v$  unchanged, and (c) the entire behavior satisfies  $L$ .

An action  $\mathcal{A}$  is said to be a *subaction* of a safety property  $\Pi$  iff for every finite behavior  $s_1, \dots, s_n$  satisfying  $\Pi$  with  $Enabled \ \mathcal{A}$  true in state  $s_n$ , there exists a state  $s_{n+1}$  such that  $(s_n, s_{n+1})$  is an  $\mathcal{A}$  step and  $s_1, \dots, s_{n+1}$  satisfies  $\Pi$ . By this definition,  $\mathcal{A}$  is a subaction of  $Init \wedge \Box[\mathcal{N}]_v$  iff<sup>2</sup>

$$Init \wedge \Box[\mathcal{N}]_v \Rightarrow \Box((Enabled \ \mathcal{A}) \Rightarrow Enabled \ (\mathcal{A} \wedge [\mathcal{N}]_v))$$

Two actions  $\mathcal{A}$  and  $\mathcal{B}$  are *disjoint for* a safety property  $\Pi$  iff no behavior satisfying  $\Pi$  contains an  $\mathcal{A} \wedge \mathcal{B}$  step. By this definition,  $\mathcal{A}$  and  $\mathcal{B}$  are disjoint for  $Init \wedge \Box[\mathcal{N}]_v$  iff

$$Init \wedge \Box[\mathcal{N}]_v \Rightarrow \Box \neg Enabled \ (\mathcal{A} \wedge \mathcal{B} \wedge [\mathcal{N}]_v)$$

The following result shows that the conjunction of WF and SF formulas is a fairness property. It is a special case of Proposition 4 of Section 4.

<sup>1</sup>One sometimes defines  $s_1, \dots, s_n$  to satisfy  $F$  iff the behavior  $s_1, \dots, s_n, s_n, s_n, \dots$  is in  $F$ . Since properties are invariant under stuttering, this alternative definition leads to the same definition of a safety property.

<sup>2</sup>We let  $\Rightarrow$  have lower precedence than the other boolean operators.



**Proposition 1** *If  $\Pi$  is a safety property and  $L$  is the conjunction of a finite or countably infinite number of formulas of the form  $\text{WF}_w(\mathcal{A})$  and/or  $\text{SF}_w(\mathcal{A})$  such that each  $\langle \mathcal{A} \rangle_w$  is a subaction of  $\Pi$ , then  $(\Pi, L)$  is machine closed.*

In practice, each  $w$  will usually be a tuple of variables changed by the corresponding action  $\mathcal{A}$ , so  $\langle \mathcal{A} \rangle_w$  will equal  $\mathcal{A}$ .<sup>3</sup> In the informal exposition, we often omit the subscript and talk about  $\mathcal{A}$  when we really mean  $\langle \mathcal{A} \rangle_w$ .

Machine closure for more general classes of properties can be proved with the following two propositions, which are proved in the appendix. To apply the first, one must prove that  $\exists x : \Pi$  is a safety property. By Proposition 2 of [2, page 265], it suffices to prove that  $\Pi$  has finite internal nondeterminism (fin), with  $x$  as its internal state component. Here, fin means roughly that there are only a finite number of sequences of values for  $x$  that can make a finite behavior satisfy  $\Pi$ .

**Proposition 2** *If  $(\Pi, L)$  is machine closed,  $x$  is a tuple of variables that do not occur free in  $L$ , and  $\exists x : \Pi$  is a safety property, then  $(\exists x : \Pi), L$  is machine closed.*

**Proposition 3** *If  $(\Pi, L_1)$  is machine closed and  $\Pi \wedge L_1$  implies  $L_2$ , then  $(\Pi, L_2)$  is machine closed.*

## 2.4 History-Determined Variables

A *history-determined* variable is one whose current value can be inferred from the current and past values of other variables. For the precise definition, let

$$\text{Hist}(h, f, g, v) \triangleq (h = f) \wedge \square[(h' = g) \wedge (v' \neq v)]_{(h,v)} \quad (4)$$

where  $f$  and  $v$  are state functions and  $g$  is a transition function. A variable  $h$  is a history-determined variable for a formula  $\Pi$  iff  $\Pi$  implies  $\text{Hist}(h, f, g, v)$ , for some  $f, g$ , and  $v$  such that  $h$  occurs free in neither  $f$  nor  $v$ , and  $h'$  does not occur free in  $g$ .

If  $f$  and  $v$  do not depend on  $h$ , and  $g$  does not depend on  $h'$ , then  $\exists h : \text{Hist}(h, f, g, v)$  is identically true. Therefore, if  $h$  does not occur free in formula  $\Phi$ , then  $\exists h : (\Phi \wedge \text{Hist}(h, f, g, v))$  is equivalent to  $\Phi$ . In other words, conjoining  $\text{Hist}(h, f, g, v)$  to  $\Phi$  does not change the behavior of its variables, so it makes  $h$  a “dummy variable” for  $\Phi$ —in fact, it is a special kind of history variable [2, page 270].

<sup>3</sup>More precisely,  $T \wedge \mathcal{A}$  will imply  $w' \neq w$ , where  $T$  is the type-correctness invariant.

As an example, we add to the lossy queue's specification  $\Phi_Q$  a history variable  $hin$  that records the sequence of values transmitted on the input wire. Let

$$\begin{aligned}
H_{in} \triangleq & \wedge hin = \langle \rangle & (5) \\
& \wedge \square[ \wedge hin' = hin \circ \langle ival' \rangle \\
& \wedge (ival, ibit)' \neq (ival, ibit) ]_{(hin, ival, ibit)}
\end{aligned}$$

Then  $H_{in}$  equals  $Hist(hin, \langle \rangle, hin \circ \langle ival' \rangle, (ival, ibit))$ , so  $hin$  is a history-determined variable for  $\Phi_Q \wedge H_{in}$ , and  $\exists hin : (\Phi_Q \wedge H_{in})$  equals  $\Phi_Q$ .

If  $h$  is a history-determined variable for a property  $\Pi$ , then  $\Pi$  is *fin*, with  $h$  as its internal state component. Hence, if  $\Pi$  is a safety property, then  $\exists h : \Pi$  is also a safety property.

### 3 Real-Time Closed Systems

We now use TLA to specify and reason about timing properties of closed systems. Section 3.1 explains how time and timing properties can be represented with TLA formulas, and Section 3.2 describes how to reason about these formulas. The problem of Zeno specifications is addressed in Section 3.3. Our method of specifying and reasoning about timing properties is illustrated in Section 3.4 with the example of a real-time mutual exclusion protocol.

#### 3.1 Time and Timers

In real-time TLA specifications, real time is represented by the variable *now*. Although it has a special interpretation, *now* is just an ordinary variable of the logic. The value of *now* is always a real number, and it never decreases—conditions expressed by the TLA formula

$$RT \triangleq (now \in \mathbf{R}) \wedge \square[now' \in (now, \infty)]_{now}$$

where  $\mathbf{R}$  is the set of real numbers and  $(r, \infty)$  is  $\{t \in \mathbf{R} : t > r\}$ .

It is convenient to make time-advancing steps distinct from ordinary program steps. This is done by strengthening the formula  $RT$  to

$$RT_v \triangleq (now \in \mathbf{R}) \wedge \square[(now' \in (now, \infty)) \wedge (v' = v)]_{now}$$

This property differs from  $RT$  only in asserting that  $v$  does not change when  $now$  advances. Simple logical manipulation shows that  $RT_v$  is equivalent to  $RT \wedge \Box[now' = now]_v$ , and

$$Init \wedge \Box[\mathcal{N}]_v \wedge RT_v = Init \wedge \Box[\mathcal{N} \wedge (now' = now)]_v \wedge RT$$

Real-time constraints are imposed by using *timers* to restrict the increase of  $now$ . A *timer for*  $\Pi$  is a state function  $t$  such that  $\Pi$  implies  $\Box(t \in \mathbf{R} \cup \{\pm\infty\})$ . Timer  $t$  is used as an upper-bound timer by conjoining the formula

$$MaxTime(t) \triangleq (now \leq t) \wedge \Box[now' \leq t']_{now}$$

to a specification. This formula asserts that  $now$  is never advanced past  $t$ . Timer  $t$  is used as a lower-bound timer for an action  $\mathcal{A}$  by conjoining the formula

$$MinTime(t, \mathcal{A}, v) \triangleq \Box[\mathcal{A} \Rightarrow (t \leq now)]_v$$

to a specification. This formula asserts that an  $\langle \mathcal{A} \rangle_v$  step cannot occur when  $now$  is less than  $t$ .<sup>4</sup>

A common type of timing constraint asserts that an  $\mathcal{A}$  step must occur within  $\delta$  seconds of when the action  $\mathcal{A}$  becomes enabled, for some constant  $\delta$ . After an  $\mathcal{A}$  step, the next  $\mathcal{A}$  step must occur within  $\delta$  seconds of when action  $\mathcal{A}$  is re-enabled. There are at least two reasonable interpretations of this requirement.

The first interpretation is that the  $\mathcal{A}$  step must occur if  $\mathcal{A}$  has been continuously enabled for  $\delta$  seconds. This is expressed by  $MaxTime(t)$  when  $t$  is a state function satisfying

$$\begin{aligned} VTimer(t, A, \delta, v) \triangleq & \wedge t = \mathbf{if} \textit{Enabled} \langle \mathcal{A} \rangle_v \mathbf{then} now + \delta \\ & \mathbf{else} \infty \\ & \wedge \Box[\wedge t' = \mathbf{if} (\textit{Enabled} \langle \mathcal{A} \rangle_v)' \\ & \mathbf{then if} \langle \mathcal{A} \rangle_v \vee \neg \textit{Enabled} \langle \mathcal{A} \rangle_v \\ & \mathbf{then} now + \delta \\ & \mathbf{else} t \\ & \mathbf{else} \infty \\ & \wedge v' \neq v ]_{(t,v)} \end{aligned}$$

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<sup>4</sup>Unlike the usual timers in computer systems that represent an increment of time, our timers represent an absolute time. To allow the type of strict time bound that would be expressed by replacing  $\leq$  with  $<$  in the definition of  $MaxTime$  or  $MinTime$ , we could introduce, as additional possible values for timers, the set of all “infinitesimally shifted” real numbers  $r^-$ , where  $t \leq r^-$  iff  $t < r$ , for any reals  $t$  and  $r$ .

Such a  $t$  is called a *volatile  $\delta$ -timer*.

Another interpretation of the timing requirement is that an  $\mathcal{A}$  step must occur if  $\mathcal{A}$  has been enabled for a total of  $\delta$  seconds, though not necessarily continuously enabled. This is expressed by  $MaxTime(t)$  when  $t$  satisfies

$$\begin{aligned}
 PTimer(t, A, \delta, v) \triangleq & \quad \wedge t = now + \delta \\
 & \quad \wedge \square[\wedge t' = \mathbf{if} \textit{Enabled} \langle \mathcal{A} \rangle_v \\
 & \quad \quad \quad \mathbf{then if} \langle \mathcal{A} \rangle_v \mathbf{then} now + \delta \\
 & \quad \quad \quad \mathbf{else} t \\
 & \quad \quad \quad \mathbf{else} t + (now' - now) \\
 & \quad \wedge (v, now)' \neq (v, now) ]_{(t,v,now)}
 \end{aligned}$$

Such a  $t$  is called a *persistent  $\delta$ -timer*. We can use  $\delta$ -timers as lower-bound timers as well as upper-bound timers.

Observe that  $VTimer(t, \mathcal{A}, \delta, v)$  has the form  $Hist(t, f, g, v)$  and that  $PTimer(t, \mathcal{A}, \delta, v)$  has the form  $Hist(t, f, g, (v, now))$ , where  $Hist$  is defined by (4). Thus, if formula  $\Pi$  implies that a variable  $t$  satisfies either of these formulas, then  $t$  is a history-determined variable for  $\Pi$ .

As an example of the use of timers, we make the lossy queue of Section 2.1 nonlossy by adding the following timing constraints.

- Values must be put on a wire at most once every  $\delta_{snd}$  seconds. There are two conditions—one on the input wire and one on the output wire. They are expressed by using  $\delta_{snd}$ -timers  $t_{Inp}$  and  $t_{DeQ}$ , for the actions  $Inp$  and  $DeQ$ , as lower-bound timers.
- A value must be added to the queue at most  $\Delta_{rcv}$  seconds after it appears on the input wire. This is expressed by using a  $\Delta_{rcv}$ -timer  $T_{EnQ}$ , for the enqueue action, as an upper-bound timer.
- A value must be sent on the output wire within  $\Delta_{snd}$  seconds of when it reaches the head of the queue. This is expressed by using a  $\Delta_{snd}$ -timer  $T_{DeQ}$ , for the dequeue action, as an upper-bound timer.

The timed queue will be nonlossy if  $\Delta_{rcv} < \delta_{snd}$ . In this case, we expect the  $Inp$ ,  $EnQ$ , and  $DeQ$  actions to remain enabled until they are “executed”, so it doesn’t matter whether we use volatile or persistent timers. We use volatile timers because they are a little easier to reason about.

The timed version  $\Pi'_Q$  of the queue's internal specification  $\Pi_Q$  is obtained by conjoining the timing constraints to  $\Pi_Q$ :

$$\begin{aligned} \Pi'_Q \triangleq & \Pi_Q \wedge RT_v & (6) \\ & \wedge VTimer(t_{Inp}, Inp, \delta_{snd}, v) \wedge MinTime(t_{Inp}, Inp, v) \\ & \wedge VTimer(t_{DeQ}, DeQ, \delta_{snd}, v) \wedge MinTime(t_{DeQ}, DeQ, v) \\ & \wedge VTimer(T_{EnQ}, EnQ, \Delta_{rcv}, v) \wedge MaxTime(T_{EnQ}) \\ & \wedge VTimer(T_{DeQ}, DeQ, \Delta_{snd}, v) \wedge MaxTime(T_{DeQ}) \end{aligned}$$

The external specification  $\Phi'_Q$  of the timed queue is obtained by existentially quantifying first the timers and then the variables  $q$  and  $last$ .

Formula  $\Pi'_Q$  of (6) is not in the canonical form for a TLA formula. A straightforward calculation, using the type-correctness invariant (1) and the equivalence of  $(\Box F) \wedge (\Box G)$  and  $\Box(F \wedge G)$ , converts the expression (6) for  $\Pi'_Q$  to the canonical form given in Figure 3.<sup>5</sup> Observe how each subaction  $\mathcal{A}$  of the original formula has a corresponding timed version  $\mathcal{A}'$ . Action  $\mathcal{A}'$  is obtained by conjoining  $\mathcal{A}$  with the appropriate relations between the old and new values of the timers. If  $\mathcal{A}$  has a lower-bound timer, then  $\mathcal{A}'$  also has a conjunct asserting that it is not enabled when *now* is less than this timer. (The lower-bound timer  $t_{Inp}$  for *Inp* does not affect the enabling of other subactions because *Inp* is disjoint from all other subactions; a similar remark applies to the lower-bound timer  $t_{DeQ}$ .) There is also a new action, *QTick*, that advances *now*.

Formula  $\Pi'_Q$  is the TLA specification of a program that satisfies each maximum-delay constraint by preventing *now* from advancing before the constraint has been satisfied. Thus, the program “implements” timing constraints by stopping time, an apparent absurdity. However, the absurdity results from thinking of a TLA formula, or the abstract program that it represents, as a *prescription of how* something is accomplished. A TLA formula is really a *description of what* is supposed to happen. Formula  $\Pi'_Q$  says only that an action occurs before *now* reaches a certain value. It is just our familiarity with ordinary programs that makes us jump to the conclusion that *now* is being changed by the system.

## 3.2 Reasoning About Time

Formula  $\Pi'_Q$  is a safety property; it is satisfied by a behavior in which no variables change values. In particular, it allows behaviors in which time stops. We can rule

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<sup>5</sup>Further simplification of this formula is possible, but it requires an invariant. In particular, the fourth conjunct of  $DeQ'$  can be replaced by  $T'_{EnQ} = T_{EnQ}$ .

$$\begin{aligned}
Init_Q^t &\stackrel{\Delta}{=} \wedge Init_Q \\
&\wedge now \in \mathbf{R} \\
&\wedge t_{Inp} = now + \delta_{snd} \\
&\wedge t_{DeQ} = T_{EnQ} = T_{DeQ} = \infty \\
Inp^t &\stackrel{\Delta}{=} \wedge Inp \\
&\wedge t_{Inp} \leq now \\
&\wedge t'_{Inp} = now' + \delta_{snd} \\
&\wedge T'_{EnQ} = \mathbf{if} \textit{last}' \neq \textit{ibit}' \mathbf{then} now' + \Delta_{rcv} \mathbf{else} \infty \\
&\wedge (t_{DeQ}, T_{DeQ})' = \mathbf{if} q = \langle\langle \rangle\rangle \mathbf{then} (\infty, \infty) \mathbf{else} (t_{DeQ}, T_{DeQ}) \\
&\wedge now' = now \\
EnQ^t &\stackrel{\Delta}{=} \wedge EnQ \\
&\wedge T'_{EnQ} = \infty \\
&\wedge (t_{DeQ}, T_{DeQ})' = \mathbf{if} q = \langle\langle \rangle\rangle \mathbf{then} (now + \delta_{snd}, now + \Delta_{snd}) \\
&\quad \mathbf{else} (t_{DeQ}, T_{DeQ}) \\
&\wedge (t_{Inp}, now)' = (t_{Inp}, now) \\
DeQ^t &\stackrel{\Delta}{=} \wedge DeQ \\
&\wedge t_{DeQ} \leq now \\
&\wedge (t_{DeQ}, T_{DeQ})' = \mathbf{if} q' = \langle\langle \rangle\rangle \mathbf{then} (\infty, \infty) \\
&\quad \mathbf{else} (now + \delta_{snd}, now + \Delta_{snd}) \\
&\wedge T'_{EnQ} = \mathbf{if} \textit{last}' = \textit{ibit}' \mathbf{then} \infty \mathbf{else} T_{EnQ} \\
&\wedge (t_{Inp}, now)' = (t_{Inp}, now) \\
QTick &\stackrel{\Delta}{=} \wedge now' \in (now, \min(T_{DeQ}, T_{EnQ})] \\
&\wedge (v, t_{Inp}, t_{DeQ}, T_{DeQ}, T_{EnQ})' = (v, t_{Inp}, t_{DeQ}, T_{DeQ}, T_{EnQ}) \\
vt &\stackrel{\Delta}{=} (v, now, t_{Inp}, t_{DeQ}, T_{DeQ}, T_{EnQ}) \\
\Pi_Q^t &\stackrel{\Delta}{=} \wedge Init_Q^t \\
&\wedge \square [Inp^t \vee EnQ^t \vee DeQ^t \vee QTick]_{vt}
\end{aligned}$$

Figure 3: The canonical form for  $\Pi_Q^t$ , where  $(r, s]$  denotes the set of reals  $u$  such that  $r < u \leq s$ .

out such behaviors by conjoining to  $\Pi'_Q$  the liveness property

$$NZ \triangleq \forall t \in \mathbf{R} : \diamond(now > t)$$

which asserts that *now* gets arbitrarily large. However, when reasoning only about real-time properties, this is not necessary. For example, suppose we want to show that our timed queue satisfies a real-time property expressed by formula  $\Psi'$ , which is also a safety property. If  $\Pi'_Q$  implies  $\Psi'$ , then  $\Pi'_Q \wedge NZ$  implies  $\Psi' \wedge NZ$ . Conversely, we don't expect conjoining a liveness property to add safety properties; if  $\Pi'_Q \wedge NZ$  implies  $\Psi'$ , then  $\Pi'_Q$  by itself should imply  $\Psi'$ —a point discussed in Section 3.3 below. Hence, there is no need to introduce the liveness property *NZ*.

A safety property we might want to prove for the timed queue is that it does not lose any inputs. To express this property, let *hin* be the history variable, determined by  $H_{in}$  of (5), that records the sequence of input values; and let *hout* and  $H_{out}$  be the analogous history variable and property for the outputs. The assertion that the timed queue loses no inputs is expressed by

$$\Pi'_Q \wedge H_{in} \wedge H_{out} \Rightarrow \square(hout \preceq hinp)$$

where  $\alpha \preceq \beta$  iff  $\alpha$  is an initial prefix of  $\beta$ . This is a standard invariance property. The usual method for proving such properties leads to the following invariant

$$\begin{aligned} & \wedge T_Q \wedge (t_{Imp}, now \in \mathbf{R}) \wedge (T_{EnQ}, t_{DeQ}, T_{DeQ} \in \mathbf{R} \cup \{\infty\}) \\ & \wedge now \leq \min(T_{EnQ}, T_{DeQ}) \\ & \wedge (last = ibit) \Rightarrow (T_{EnQ} = \infty) \wedge (hinp = hout \circ q) \\ & \wedge (last \neq ibit) \Rightarrow (T_{EnQ} < t_{Imp}) \wedge (hinp = hout \circ q \circ \langle\langle ival \rangle\rangle) \\ & \wedge (q = \langle\langle \rangle\rangle) \equiv (T_{DeQ} = \infty) \end{aligned}$$

and to the necessary assumption  $\Delta_{rcv} < \delta_{snd}$ . (Recall that  $T_Q$  is the type-correctness predicate (1) for  $\Pi_Q$ .)

Property *NZ* is needed to prove that real-time properties imply liveness properties. The desired liveness property for the timed queue is that the sequence of input messages up to any point eventually appears as the sequence of output messages. It is expressed by

$$\Pi'_Q \wedge NZ \Rightarrow \forall \sigma : \square((hinp = \sigma) \Rightarrow \diamond(hout = \sigma))$$

This formula is proved by first showing

$$\Pi'_Q \wedge NZ \Rightarrow \text{WF}_v(EnQ) \wedge \text{WF}_v(DeQ) \quad (7)$$

and then using a standard liveness argument to prove

$$\Pi'_Q \wedge \text{WF}_v(\text{En}Q) \wedge \text{WF}_v(\text{De}Q) \Rightarrow \forall \sigma : \Box((\text{hinp} = \sigma) \Rightarrow \Diamond(\text{hout} = \sigma))$$

The proof that  $\Pi'_Q \wedge \text{NZ}$  implies  $\text{WF}_v(\text{En}Q)$  is by contradiction. Assume  $\text{En}Q$  is forever enabled but never occurs. An invariance argument then shows that  $\Pi'_Q$  implies that  $T_{\text{En}Q}$  forever equals its current value, preventing  $\text{now}$  from advancing past that value; and this contradicts  $\text{NZ}$ . The proof that  $\Pi'_Q \wedge \text{NZ}$  implies  $\text{WF}_v(\text{De}Q)$  is similar.

### 3.3 The NonZeno Condition

The timed queue specification  $\Pi'_Q$  asserts that a  $\text{De}Q$  action must occur between  $\delta_{\text{snd}}$  and  $\Delta_{\text{snd}}$  seconds of when it becomes enabled. What if  $\Delta_{\text{snd}} < \delta_{\text{snd}}$ ? If an input occurs, it eventually is put in the queue, enabling  $\text{De}Q$ . At that point, the value of  $\text{now}$  can never become more than  $\Delta_{\text{snd}}$  greater than its current value, so the program eventually reaches a “time-blocked state”. In a time-blocked state, only the  $\text{QTick}$  action can be enabled, and it cannot advance  $\text{now}$  past some fixed time. In other words, eventually a state is reached in which every variable other than  $\text{now}$  remains the same, and  $\text{now}$  either remains the same or keeps advancing closer and closer to some upper bound.

We can attempt to correct such pathological specifications by requiring that  $\text{now}$  increase without bound. This is easily done by conjoining the liveness property  $\text{NZ}$  to the safety property  $\Pi'_Q$ , but that doesn’t accomplish anything. Since  $\Pi'_Q \wedge \text{NZ}$  rules out behaviors in which  $\text{now}$  is bounded, it allows only behaviors in which there is no input, if  $\Delta_{\text{snd}} < \delta_{\text{snd}}$ . Such a specification is no better than the original specification  $\Pi'_Q$ . The fact that the safety property allows the possibility of reaching a time-blocked state indicates an error in the specification. One does not add timing constraints on output actions with the intention of forbidding input.

We call a safety property *Zeno* if it allows the system to reach a state from which  $\text{now}$  must remain bounded. More precisely, a safety property  $\Pi$  is *nonZeno* iff every finite behavior satisfying  $\Pi$  can be completed to an infinite behavior satisfying  $\Pi$  in which  $\text{now}$  increases without bound. In other words,  $\Pi$  is *nonZeno* iff the pair  $(\Pi, \text{NZ})$  is machine closed.<sup>6</sup>

Zeno specifications can be a source of incompleteness for proof methods. Only *nonZeno* behaviors are physically meaningful, so a real-time system with specifi-

<sup>6</sup>An arbitrary property  $\Pi$  is *nonZeno* iff  $(\mathcal{C}(\Pi), \Pi \wedge \text{NZ})$  is machine closed. We restrict our attention to real-time constraints for safety specifications.



cation  $\Pi$  satisfies a property  $\Psi$  if  $\Pi \wedge NZ \Rightarrow \Psi$ . Most methods for proving safety properties use only safety properties as hypotheses, so they can prove  $\Pi \wedge NZ \Rightarrow \Psi$  for safety properties  $\Pi$  and  $\Psi$  only by proving  $\Pi \Rightarrow \Psi$ . NonZenoness of  $\Pi$  means that  $\Pi \wedge NZ \Rightarrow \Psi$  holds iff  $\Pi \Rightarrow \Psi$  does. However, if  $\Pi$  is Zeno, then  $\Pi \wedge NZ \Rightarrow \Psi$  could hold even though  $\Pi \Rightarrow \Psi$  does not, and these methods will be unable to prove that the system with specification  $\Pi$  satisfies  $\Psi$ . NonZenoness is therefore required for completeness.

The following result can be used to ensure that a real-time specification written in terms of volatile  $\delta$ -timers is nonZeno.

**Theorem 1** *Let  $v$  be the tuple of variables free in  $Init$  or  $\mathcal{N}$ . The property*

$$\begin{aligned} & \wedge Init \wedge \square[\mathcal{N}]_v \wedge RT_v \\ & \wedge \forall i \in I : VTimer(t_i, \mathcal{A}_i, \delta_i, v) \wedge MinTime(t_i, \mathcal{A}_i, v) \\ & \wedge \forall j \in J : VTimer(T_j, \mathcal{A}_j, \Delta_j, v) \wedge MaxTime(T_j) \end{aligned}$$

*is nonZeno if now does not appear in  $v$ ,  $I$  and  $J$  are finite sets, and for all  $i \in I$  and  $j \in J$ :*

1.  $\langle \mathcal{A}_j \rangle_v$  is a subaction of  $Init \wedge \square[\mathcal{N}]_v$  whose free variables appear in  $v$ ,
2.  $\langle \mathcal{A}_i \rangle_v$  and  $\langle \mathcal{A}_j \rangle_v$  are disjoint for  $Init \wedge \square[\mathcal{N}]_v$  if  $i \neq j$ ,
3.  $\delta_i$  and  $\Delta_j$  are positive reals and, if  $i = j$ , then  $\delta_i \leq \Delta_j$ ,
4. the  $t_i$  and  $T_j$  are distinct variables different from now and from the variables in  $v$ .

We can apply the theorem to prove that the specification  $\Pi_Q^t$  is nonZeno if  $\delta_{snd} \leq \Delta_{snd}$ . The hypotheses of the theorem are checked as follows.

1. Actions  $\langle DeQ \rangle_v$  and  $\langle EnQ \rangle_v$  imply  $\mathcal{N}_Q$ , so they are subactions of  $\Pi_Q$ .
2. The conjunction of any two of the actions  $\langle Inp \rangle_v$ ,  $\langle DeQ \rangle_v$ , and  $\langle EnQ \rangle_v$  equals false, so the actions are pairwise disjoint for  $\Pi_Q$ .<sup>7</sup>
3. The hypothesis  $\delta_{snd} \leq \Delta_{snd}$  is used here.

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<sup>7</sup>Actually, the type-correctness predicate  $T_Q$  is needed to prove that  $\langle Inp \rangle_v \wedge \langle DeQ \rangle_v$  equals false.

4. Trivial.

The theorem is valid for persistent as well as volatile timers. Any combination of *VTimer* and *PTimer* formulas may occur, except that a single  $\mathcal{A}_k$  cannot have a persistent lower-bound timer  $t_k$  and a volatile upper-bound timer  $T_k$ . All of these results are corollaries of the following theorem, which in turn is a consequence of Theorem 4 of Section 4.

**Theorem 2** *Let*

- $\Pi$  be a safety property of the form  $\text{Init} \wedge \Box[\mathcal{N}]_w$ ,
- $t_i$  and  $T_j$  be timers for  $\Pi$  and let  $\mathcal{A}_k$  be an action, for all  $i \in I$ ,  $j \in J$ , and  $k \in I \cup J$ , where  $I$  and  $J$  are sets, with  $J$  finite,
- $\Pi' \triangleq \Pi \wedge RT_v \wedge \forall i \in I : \text{MinTime}(t_i, \mathcal{A}_i, v) \wedge \forall j \in J : \text{MaxTime}(T_j)$

If 1.  $\langle \mathcal{A}_i \rangle_v$  and  $\langle \mathcal{A}_j \rangle_v$  are disjoint for  $\Pi$ , for all  $i \in I$  and  $j \in J$  with  $i \neq j$ ,

now

2. (a) *does not occur free in v*,  
(b)  $(\text{now}' = r) \wedge (v' = v)$  is a subaction of  $\Pi$ , for all  $r \in \mathbf{R}$ ,
3. for all  $j \in J$ :  
(a)  $\langle \mathcal{A}_j \rangle_v \wedge (\text{now}' = \text{now})$  is a subaction of  $\Pi$ .  
(b)  $\Pi \Rightarrow \text{VTimer}(T_j, \mathcal{A}_j, \Delta_j, v)$ , or  
 $\Pi \Rightarrow \text{PTimer}(T_j, \mathcal{A}_j, \Delta_j, v)$ , where  $\Delta_j \in (0, \infty)$ ,  
(c)  $\Pi' \Rightarrow \Box(\text{Enabled } \langle \mathcal{A}_j \rangle_v = \text{Enabled } (\langle \mathcal{A}_j \rangle_v \wedge (\text{now}' = \text{now})))$   
(d)  $(v' = v) \Rightarrow (\text{Enabled } \langle \mathcal{A}_j \rangle_v = (\text{Enabled } \langle \mathcal{A}_j \rangle_v)')$
4.  $\Pi' \Rightarrow \Box(t_k \leq T_k)$ , for all  $k \in I \cap J$ ,

then  $(\Pi', NZ)$  is machine closed

Most nonaxiomatic approaches, including both real-time process algebras and more traditional programming languages with timing constraints, essentially use  $\delta$ -timers for actions. Theorem 2 implies that they automatically yield nonZeno specifications.

Theorem 2 can be further generalized in two ways. First,  $J$  can be infinite—if  $\Pi'$  implies that only a finite number of actions  $\mathcal{A}_j$  with  $j \in J$  are enabled before time  $r$ , for any  $r \in \mathbf{R}$ . For example, by letting  $\mathcal{A}_j$  be the action that sends message number  $j$ , we can apply the theorem to a program that sends messages number 1 through  $n$  at time  $n$ , for every integer  $n$ . This program is nonZeno even though the number of actions per second that it performs is unbounded. Second, we can extend the theorem to the more general class of timers obtained by letting the  $\Delta_j$  be arbitrary real-valued state functions, rather than just constants—if all the  $\Delta_j$  are bounded from below by a positive constant  $\Delta$ .

Theorem 2 can be proved using Propositions 1 and 3 and ordinary TLA reasoning. By these propositions, it suffices to display a formula  $L$  that is the conjunction of fairness conditions on subactions of  $\Pi'$  such that  $\Pi' \wedge L$  implies  $NZ$ . A suitable  $L$  is  $\text{WF}_{(now,v)}(\mathcal{C})$ , where  $\mathcal{C}$  is an action that either (a) advances  $now$  by  $\min_{j \in J} \Delta_j$  if allowed by the upper-bound timers  $T_j$ , or else as far as they do allow, or (b) executes an  $\langle \mathcal{A}_j \rangle_v$  action for which  $now = T_j$ . The proof in the appendix of Theorem 4, which implies Theorem 2, generalizes this approach.

Theorem 2 does not cover all situations of interest. For example, one can require of our timed queue that the first value appear on the output line within  $\epsilon$  seconds of when it is placed on the input line. This effectively places an upper bound on the sum of the times needed for performing the  $EnQ$  and  $DeQ$  actions; it cannot be expressed with  $\delta$ -timers on individual actions. For these general timing constraints, nonZenoness must be proved for the individual specification. The proof uses the method described above for proving Theorem 2: we add to the timed program  $\Pi'$  a liveness property  $L$  that is the conjunction of any fairness properties we like, including fairness of the action that advances  $now$ , and prove that  $\Pi' \wedge L$  implies  $NZ$ . NonZenoness then follows from Propositions 1 and 3.

There is another possible approach to proving nonZenoness. One can make granularity assumptions—lower bounds both on the amount by which  $now$  is incremented and on the minimum delay for each action. Under these assumptions, nonZenoness is equivalent to the absence of deadlock, which can be proved by existing methods. Granularity assumptions are probably adequate—after all, what harm can come from pretending that nothing happens in less than  $10^{-100}$  nanoseconds? However, they can be unnatural and cumbersome. For example, distributed algorithms often assume that only message delays are significant, so the time required for local actions is ignored. The specification of such an algorithm should place no lower bound on the time required for a local action, but that would violate any granularity assumptions. We believe that any proof of deadlock freedom based on granularity

can be translated into a proof of nonZenoness using the method outlined above.

So far, we have been discussing nonZenoness of the internal specification, where both the timers and the system's internal variables are visible. Timers are defined by adding history-determined variables, so existentially quantifying over them preserves nonZenoness by Proposition 2. We expect most specifications to be fin [2, page 263], so nonZenoness will also be preserved by existentially quantifying over the system's internal variables. This is the case for the timed queue.

### 3.4 An Example: Fischer's Protocol

As another example of real-time closed systems, we treat a simplified version of a real-time mutual exclusion protocol proposed by Fischer [9], [12, page 2]. The example was suggested by Schneider [18]. The protocol consists of each process  $i$  executing the following code, where angle brackets denote instantaneous atomic actions:

```
a:  await  $\langle x = 0 \rangle$ ;  
b:   $\langle x := i \rangle$ ;  
c:  await  $\langle x = i \rangle$ ;  
cs: critical section
```

There is a maximum delay  $\Delta_b$  between the execution of the test in statement  $a$  and the assignment in statement  $b$ , and a minimum delay  $\delta_c$  between the assignment in statement  $b$  and the test in statement  $c$ . The problem is to prove that, with suitable conditions on  $\Delta_b$  and  $\delta_c$ , this protocol guarantees mutual exclusion (at most one process can enter its critical section).

As written, Fischer's protocol permits only one process to enter its critical section one time. The protocol can be converted to an actual mutual exclusion algorithm. The correctness proof of the protocol is easily extended to a proof of such an algorithm.

The TLA specification of the protocol is given in Figure 4. The formula  $\Pi_F$  describing the untimed version is standard TLA. We assume a finite set **Proc** of processes. Variable  $x$  represents the program variable  $x$ , and variable  $pc$  represents the control state. The value of  $pc$  will be an array indexed by **Proc**, where  $pc[i]$  equals one of the strings "a", "b", "c", "cs" when control in process  $i$  is at the corresponding statement. The initial predicate  $Init_F$  asserts that  $pc[i]$  equals "a" for each process  $i$ , so the processes start with control at statement  $a$ . No assumption on the initial value of  $x$  is needed to prove mutual exclusion.

Next come the definitions of the three actions corresponding to program statements

$$\begin{aligned}
Init_F &\triangleq \forall i \in \mathbf{Proc} : pc[i] = \mathbf{a} \\
Go(i, u, v) &\triangleq \wedge pc[i] = u \\
&\quad \wedge pc'[i] = v \\
&\quad \wedge \forall j \in \mathbf{Proc} : (j \neq i) \Rightarrow (pc'[j] = pc[j]) \\
\mathcal{A}_i &\triangleq Go(i, \mathbf{a}, \mathbf{b}) \wedge (x = x' = 0) \\
\mathcal{B}_i &\triangleq Go(i, \mathbf{b}, \mathbf{c}) \wedge (x' = i) \\
\mathcal{C}_i &\triangleq Go(i, \mathbf{c}, \mathbf{cs}) \wedge (x = x' = i) \\
\mathcal{N}_F &\triangleq \exists i \in \mathbf{Proc} : (\mathcal{A}_i \vee \mathcal{B}_i \vee \mathcal{C}_i) \\
\Pi_F &\triangleq Init_F \wedge \square[\mathcal{N}_F]_{(x, pc)} \\
\Pi_F^t &\triangleq \wedge \Pi_F \wedge RT_{(x, pc)} \\
&\quad \wedge \forall i \in \mathbf{Proc} : \wedge VTimer(T_b[i], \mathcal{B}_i, \Delta_b, (x, pc)) \\
&\quad \quad \wedge MaxTime(T_b[i]) \\
&\quad \wedge \forall i \in \mathbf{Proc} : \wedge VTimer(t_c[i], Go(i, \mathbf{c}, \mathbf{cs}), \delta_c, (x, pc)) \\
&\quad \quad \wedge MinTime(t_c[i], \mathcal{C}_i, (x, pc)) \\
\Phi_F^t &\triangleq \exists T_b, t_c : \Pi_F^t
\end{aligned}$$

Figure 4: The TLA specification of Fischer’s real-time mutual exclusion protocol.

$a$ ,  $b$ , and  $c$ . They are defined using the formula  $Go$ , where  $Go(i, u, v)$  asserts that control in process  $i$  changes from  $u$  to  $v$ , while control remains unchanged in the other processes. Action  $\mathcal{A}_i$  represents the execution of statement  $a$  by process  $i$ ; actions  $\mathcal{B}_i$  and  $\mathcal{C}_i$  have the analogous interpretation. In this simple protocol, a process stops when it gets to its critical section, so there are no other actions. The program’s next-state action  $\mathcal{N}_F$  is the disjunction of all these actions. Formula  $\Pi_F$  asserts that all processes start at statement  $a$ , and every step consists of executing the next statement of some process.

Action  $\mathcal{B}_i$  is enabled by the execution of action  $\mathcal{A}_i$ . Therefore, the maximum delay of  $\Delta_b$  between the execution of statements  $a$  and  $b$  can be expressed by an upper-bound constraint on a volatile  $\Delta_b$ -timer for action  $\mathcal{B}_i$ . The variable  $T_b$  is an array of such timers, where  $T_b[i]$  is the timer for action  $\mathcal{B}_i$ .

The constant  $\delta_c$  is the minimum delay between when control reaches statement  $c$  and when that statement is executed. Therefore, we need an array  $t_c$  of lower-bound timers for the actions  $\mathcal{C}_i$ . The delay is measured from the time control reaches

statement  $c$ , so we want  $t_c[i]$  to be a  $\delta_c$ -timer on an action that becomes enabled when process  $i$  reaches statement  $c$  and is not executed until  $\mathcal{C}_i$  is. A suitable choice for this action is  $Go(i, \text{"c"}, \text{"cs"})$ .

Adding these timers and timing constraints to the untimed formula  $\Pi_F$  yields formula  $\Pi'_F$  of Figure 4, the specification of the real-time protocol with the timers visible. The final specification,  $\Phi'_F$ , is obtained by quantifying over the timer variables  $T_b$  and  $t_c$ . Since  $\mathcal{B}_j$  is a subaction of  $\Pi_F$  and  $pc[i] = \text{"c"}$  is disjoint from  $\mathcal{B}_j$ , for all  $i$  and  $j$  in **Proc**, Theorem 2 implies that  $\Pi'_F$  is nonZeno if  $\Delta_b$  is positive. Proposition 2 can then be applied to prove that  $\Phi'_F$  is nonZeno.

Mutual exclusion asserts that two processes cannot be in their critical sections at the same time. It is expressed by the predicate

$$Mutex \triangleq \forall i, j \in \mathbf{Proc} : (pc[i] = pc[j] = \text{"cs"}) \Rightarrow (i = j)$$

The property to be proved is

$$Assump \wedge \Phi'_F \Rightarrow \Box Mutex \quad (8)$$

where *Assump* expresses the assumptions about the constants **Proc**,  $\Delta_b$ , and  $\delta_c$  needed for correctness. Since the timer variables do not occur in *Mutex* or *Assump*, (8) is equivalent to

$$Assump \wedge \Pi'_F \Rightarrow \Box Mutex$$

The standard method for proving this kind of invariance property leads to the invariant

$$\begin{aligned} & \wedge now \in \mathbf{R} \\ & \wedge \forall i \in \mathbf{Proc} : \\ & \quad \wedge T_b[i], t_c[i] \in \mathbf{R} \cup \{\infty\} \\ & \quad \wedge pc[i] \in \{\text{"a"}, \text{"b"}, \text{"c"}, \text{"cs"}\} \\ & \quad \wedge (pc[i] = \text{"cs"}) \Rightarrow \wedge x = i \\ & \quad \quad \quad \wedge \forall j \in \mathbf{Proc} : pc[j] \neq \text{"b"} \\ & \quad \wedge (pc[i] = \text{"c"}) \Rightarrow \wedge x \neq 0 \\ & \quad \quad \quad \wedge \forall j \in \mathbf{Proc} : (pc[j] = \text{"b"}) \Rightarrow (t_c[i] > T_b[j]) \\ & \quad \wedge (pc[i] = \text{"b"}) \Rightarrow (T_b[i] < now + \delta_c) \\ & \quad \wedge now \leq T_b[i] \end{aligned}$$

and the assumption

$$Assump \triangleq (0 \notin \mathbf{Proc}) \wedge (\Delta_b, \delta_c \in \mathbf{R}) \wedge (\Delta_b < \delta_c)$$

## 4 Open Systems

A closed system is solipsistic. An open system interacts with an environment, where system steps are distinguished from environment steps. Sections 4.1 and 4.2 reformulate a number of concepts introduced in [1] that are needed for treating open systems in TLA. Some new results appear in Section 4.3. The following two sections explain how reasoning about open systems is reduced to reasoning about closed systems, and how open systems are composed.

### 4.1 Receptiveness and Realizability

To describe an open system in TLA, one defines an action  $\mu$  such that  $\mu$  steps are attributed to the system and  $\neg\mu$  steps are attributed to the environment. A specification should constrain only system steps, not environment steps.

For safety properties, the concept of constraining is formalized as follows: if  $\mu$  is an action and  $\Pi$  a safety property, then  $\Pi$  *constrains at most*  $\mu$  iff, for any finite behavior  $s_1, \dots, s_n$  and state  $s_{n+1}$ , if  $s_1, \dots, s_n$  satisfies  $\Pi$  and  $(s_n, s_{n+1})$  is a  $\neg\mu$  step, then  $s_1, \dots, s_{n+1}$  satisfies  $\Pi$ . The generalization to arbitrary properties of constraining at most  $\mu$  is  $\mu$ -*receptiveness*. Intuitively,  $\Pi$  is  $\mu$ -receptive iff every behavior in  $\Pi$  can be achieved by an implementation that performs only  $\mu$  steps—the environment being able to perform any  $\neg\mu$  step. The concept of receptiveness is due to Dill [8]. The generalization to  $\mu$ -receptiveness is developed in [1].<sup>8</sup> A safety property is  $\mu$ -receptive iff it constrains at most  $\mu$ .

The generalization of machine closure to open systems is *machine realizability*. Intuitively,  $(\Pi, L)$  is  $\mu$ -machine realizable iff an implementation that performs only  $\mu$  steps can ensure that any finite behavior satisfying  $\Pi$  is completed to an infinite behavior satisfying  $\Pi \wedge L$ . Formally,  $(\Pi, L)$  is defined to be  $\mu$ -machine realizable iff  $(\Pi, L)$  is machine closed and  $\Pi \wedge L$  is  $\mu$ -receptive. For  $\mu$  equal to **true**, machine realizability reduces to machine closure.

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<sup>8</sup>To translate from the semantic model of [1] into that of TLA, we let agents be pairs of states and identify an action  $\mu$  with the set of all agents that are  $\mu$  steps. A TLA behavior  $s_1, s_2, \dots$  corresponds to the sequence  $s_1 \xrightarrow{\alpha_2} s_2 \xrightarrow{\alpha_3} s_3 \xrightarrow{\alpha_4} \dots$ , where  $\alpha_i$  equals  $(s_{i-1}, s_i)$ . With this translation, the definitions in [1] differ from the ones given here and in the appendix mainly by attributing the choice of initial state to the environment rather than to the system, requiring initial conditions to be assumptions about the environment rather than guarantees by the system.

## 4.2 The $\rightarrow$ Operator

A common way of specifying an open system is in terms of assumptions and guarantees [10], requiring the system to guarantee a property  $M$  if its environment satisfies an assumption  $E$ . An obvious formalization of such a specification is the property  $E \Rightarrow M$ . However, this property contains behaviors in which the system violates  $M$  and then the environment later violates  $E$ . Because the system cannot predict what the environment will do, such behaviors cannot occur in any actual implementation. A behavior  $\sigma$  generated by any implementation satisfies the additional property that if any finite prefix of  $\sigma$  satisfies  $E$ , then it satisfies  $M$ . We can therefore formalize the assumption/guarantee specification by the property  $E \rightarrow M$ , defined by:  $\sigma \in E \rightarrow M$  iff  $\sigma \in (E \Rightarrow M)$  and, for every finite prefix  $\rho$  of  $\sigma$ , if  $\rho$  satisfies  $E$  then  $\rho$  satisfies  $M$ . If  $E$  and  $M$  are safety properties, then  $E \rightarrow M$  is as well.

For safety properties, the operator  $\rightarrow$  is the implication operator of an intuitionistic logic [3]. Most valid propositional formulas without negation remain valid when  $\Rightarrow$  is replaced by  $\rightarrow$ , if all the formulas that appear on the left of a  $\rightarrow$  are safety properties. For example, the following formulas are valid if  $\Phi$  and  $\Pi$  are safety properties.

$$\begin{aligned} \Phi \rightarrow (\Pi \rightarrow \Psi) &\equiv (\Phi \wedge \Pi) \rightarrow \Psi & (9) \\ (\Phi \rightarrow \Psi) \wedge (\Pi \rightarrow \Psi) &\equiv (\Phi \vee \Pi) \rightarrow \Psi \end{aligned}$$

For any TLA formulas  $\Phi$  and  $\Pi$ , the property  $\Phi \rightarrow \Pi$  is expressible as a TLA formula.

## 4.3 Proving Machine Realizability

Propositions 1–3, which concern machine closure, have generalizations for machine realizability. Proposition 1 is the special case of Proposition 4 in which  $\Phi$  and  $\mu$  are identically true. Proposition 3 is similarly a special case of Proposition 5 if  $(\text{true}, L_2)$  is machine closed—that is, if  $L_2$  is a liveness property. This is sufficient for our purposes, since  $NZ$  is a liveness property. The generalization of Proposition 2 is omitted; it would be analogous to Proposition 10 of [1].

Proposition 4 is stated in terms of  $\mu$ -invariance, which generalizes the ordinary concept of invariance. A predicate  $P$  is a  $\mu$ -invariant of a formula  $\Pi$  iff, in any behavior satisfying  $\Pi$ , no  $\mu$ -step makes  $P$  false. This condition is expressed by the TLA formula  $\Pi \Rightarrow \Box[(\mu \wedge P) \Rightarrow P']_P$ .



**Proposition 4** *If  $\Pi$  and  $\Phi$  are safety properties,  $\Pi$  constrains at most  $\mu$ , and  $L$  is the conjunction of a finite or countably infinite number of formulas of the form  $\text{WF}_w(\mathcal{A})$  and/or  $\text{SF}_w(\mathcal{A})$ , where, for each such formula,*

1.  $\langle \mathcal{A} \rangle_w$  is a subaction of  $\Pi \wedge \Phi$ ,
2.  $\Pi \wedge \Phi \Rightarrow \Box[\langle \mathcal{A} \rangle_w \Rightarrow \mu]_w$ ,
3. if  $\mathcal{A}$  appears in a formula  $\text{SF}_w(\mathcal{A})$ , then  $\text{Enabled } \langle \mathcal{A} \rangle_w$  is a  $\neg\mu$ -invariant of  $\Pi \wedge \Phi$ ,

then  $(\Phi \rightarrow \Pi, \Phi \Rightarrow L)$  is  $\mu$ -machine realizable.

**Proposition 5** *If  $\Phi$  and  $\Pi$  are safety properties,  $(\Phi \rightarrow \Pi, L_1)$  and  $(\text{true}, L_2)$  are  $\mu$ -machine realizable, and  $\Phi \wedge \Pi \wedge L_1$  implies  $L_2$ , then  $(\Phi \rightarrow \Pi, L_2)$  is  $\mu$ -machine realizable.*

#### 4.4 Reduction to Closed Systems

Consider a specification  $E \rightarrow M$ , where  $E$  and  $M$  are safety properties. We expect the system's requirement to restrict only system steps, meaning that  $M$  constrains at most  $\mu$ . This implies that  $E \rightarrow M$  also constrains at most  $\mu$ . We also expect the environment assumption  $E$  not to constrain system steps; formally,  $E$  does not constrain  $\mu$  iff it constrains at most  $\neg\mu$  and it is satisfied by every (finite behavior consisting only of an) initial state.<sup>9</sup>

Suppose  $E$  and  $M$  have the following form:

$$\begin{aligned} E &\triangleq \Box[\mu \vee \mathcal{N}_E]_v \\ M &\triangleq \text{Init} \wedge \Box[\neg\mu \vee \mathcal{N}_M]_v \end{aligned}$$

Then  $E$  does not constrain  $\mu$  and  $M$  constrains at most  $\mu$ . If the system's next-state action  $\mathcal{N}_M$  implies  $\mu$ , and the environment's next-state action  $\mathcal{N}_E$  implies  $\neg\mu$ , then a simple calculation shows that

$$E \wedge M \equiv \text{Init} \wedge \Box[\mathcal{N}_E \vee \mathcal{N}_M]_v \quad (10)$$

Conjunction represents parallel composition, so  $E \wedge M$  is the formula describing the closed system consisting of the open system together with its environment. Observe

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<sup>9</sup>The asymmetry between *constrains at most* and *does not constrain* arises because we assign the system responsibility for the initial state.

that  $E \wedge M$  has precisely the form we expect for a closed system comprising two components with next-state actions  $\mathcal{N}_E$  and  $\mathcal{N}_M$ .

We can make the inverse transformation from a closed system specification  $\Pi$  to the corresponding assumption/guarantee specification  $E \rightarrow M$  such that  $\Pi$  equals  $E \wedge M$ , where  $E$  does not constrain  $\mu$  and  $M$  constrains at most  $\mu$ . This is possible because any safety property  $\Pi$  can be written as such a conjunction.

Implementation means implication. A system with guarantee  $M$  implements a system with guarantee  $\widehat{M}$ , under environment assumption  $E$ , iff  $E \rightarrow M$  implies  $E \rightarrow \widehat{M}$ . When  $E$  and  $M$  are safety properties,  $E \rightarrow M$  implies  $E \rightarrow \widehat{M}$  iff  $E \wedge M$  implies  $E \wedge \widehat{M}$ . Thus, proving that one open system implements another is equivalent to proving the implementation relation for the corresponding closed systems. Implementation for open systems therefore reduces to implementation for closed systems.

## 4.5 Composition

The distinguishing feature of open systems is that they can be composed. The proof that the composition of two specifications implements a third specification is based on the following result, which is a reformulation of Theorem 2 of [1] for safety properties.

**Theorem 3** *If  $E$ ,  $E_1$ ,  $E_2$ ,  $M_1$ , and  $M_2$  are safety properties and  $\mu_1$  and  $\mu_2$  are actions such that*

1.  $E_1$  does not constrain  $\mu_1$  and  $E_2$  does not constrain  $\mu_2$ ,
2.  $M_1$  constrains at most  $\mu_1$  and  $M_2$  constrains at most  $\mu_2$ ,

*then the following proof rule is valid:*

$$\frac{E \wedge M_1 \wedge M_2 \Rightarrow E_1 \wedge E_2}{(E_1 \rightarrow M_1) \wedge (E_2 \rightarrow M_2) \Rightarrow (E \rightarrow M_1 \wedge M_2)}$$

This theorem is essentially the same as Theorem 1 of [3]; the proof is omitted.

## 5 Real-Time Open Systems

In Section 3, we saw how we can represent time by the variable *now* and introduce timing constraints with timers. To extend the method to open systems, we need

only decide how to separate timing properties into environment assumptions and system guarantees. An examination of a paradoxical example in Section 5.1 leads to the general form described in Section 5.2, where the concept of nonZenoness is generalized.

## 5.1 A Paradox

Consider the two components  $\Pi_1$  and  $\Pi_2$  of Figure 5. Let the specification of  $\Pi_1$  be  $P_y \rightarrow P_x$ , which asserts that it writes a “good” sequence of outputs on  $x$  if its environment writes a good sequence of inputs on  $y$ . Let  $P_x \rightarrow P_y$  be the specification of  $\Pi_2$ , so  $\Pi_2$  writes a good sequence of outputs on  $y$  if its environment writes a good sequence of inputs on  $x$ . If  $P_x$  and  $P_y$  are safety properties, then it appears that we should be able to apply Theorem 3, our composition principle, to deduce that the composite system  $\Pi_{12}$  satisfies  $P_x \wedge P_y$ , producing good sequences of values on  $x$  and  $y$ . (We can define  $\mu_1$  and  $\mu_2$  so that writing on  $x$  is a  $\mu_1$  action and writing on  $y$  is a  $\mu_2$  action.)

Now, suppose  $P_x$  and  $P_y$  both assert that the value 0 is written by noon. These can be regarded as safety properties, since they assert that an undesirable event never occurs—namely, noon passing without a 0 having been written. Hence, the composition principle apparently asserts that  $\Pi_{12}$  sends 0’s along both  $x$  and  $y$  by noon. However, the specifications of  $\Pi_1$  and  $\Pi_2$  are satisfied by systems that wait for a 0 to be input, whereupon they immediately output a 0. The composition of those two systems does nothing.

This paradox depends on the ability of a system to respond instantaneously to an input. It is tempting to rule out such systems—perhaps even to outlaw specifications like these. We show that this Draconian measure is unnecessary. Indeed, if the specification of  $\Pi_2$  is strengthened to assert that a 0 must unconditionally be written on  $y$  by noon, then there is no paradox, and the composition does guarantee that a 0 is written on both  $x$  and  $y$  by noon. All paradoxes disappear when one carefully examines how the specifications must be written.

To resolve the paradox, we examine more closely the specifications  $S_1$  and  $S_2$  of  $\Pi_1$  and  $\Pi_2$ . For simplicity, let the only possible output actions be the setting of  $x$  and  $y$  to 0. The untimed version of  $S_1$  then asserts that, if the environment does nothing but set  $y$  to 0, then the system does nothing but set  $x$  to 0. This is expressed

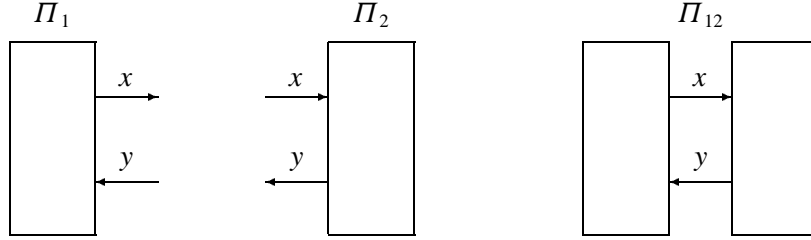


Figure 5: The composition of two systems.

in TLA by letting

$$\begin{aligned} \mathcal{M}_x &\triangleq (x' = 0) \wedge (y' = y) & \nu_1 &\triangleq x' \neq x \\ \mathcal{M}_y &\triangleq (y' = 0) \wedge (x' = x) \end{aligned}$$

and defining the untimed version of specification  $S_1$  to be

$$\Box[\nu_1 \vee \mathcal{M}_y]_{(x,y)} \rightarrow \Box[\neg\nu_1 \vee \mathcal{M}_x]_{(x,y)} \quad (11)$$

To add timing constraints, we must first decide whether the system or the environment should change *now*. Since the advancing of *now* is a mythical action that does not have to be performed by any device, either decision is possible. Somewhat surprisingly, it turns out to be more convenient to let the system advance time. With the convention that initial conditions appear in the system guarantee, we define:

$$\begin{aligned} \mathcal{N}_x &\triangleq \mathcal{M}_x \wedge (now' = now) & MT_x &\triangleq MaxTime(T_x) \\ \mathcal{N}_y &\triangleq \mathcal{M}_y \wedge (now' = now) & MT_y &\triangleq MaxTime(T_y) \\ T_x &\triangleq \mathbf{if } x \neq 0 \mathbf{ then } 12 \mathbf{ else } \infty & \mu_1 &\triangleq \nu_1 \vee (now' \neq now) \\ T_y &\triangleq \mathbf{if } y \neq 0 \mathbf{ then } 12 \mathbf{ else } \infty \\ E_1 &\triangleq \Box[\mu_1 \vee \mathcal{N}_y]_{(x,y,now)} \\ M_1 &\triangleq (now = 0) \wedge \Box[\neg\mu_1 \vee \mathcal{N}_x]_{(x,y,now)} \wedge RT_{(x,y)} \wedge MT_x \end{aligned}$$

Adding timing constraints to (11) the same way we did for closed systems then leads to the following timed version of specification  $S_1$ .

$$(E_1 \wedge MT_y) \rightarrow M_1 \quad (12)$$

However, this does not have the right form for an open system specification because  $MT_y$  constrains the advance of *now*, so the environment assumption constrains  $\mu_1$ . The conjunct  $MT_y$  must be moved from the environment assumption to the system guarantee. Using (9), we rewrite (12) as:

$$S_1 \stackrel{\Delta}{=} E_1 \rightarrow (MT_y \rightarrow M_1)$$

This has the expected form for an open system specification, with an environment assumption  $E_1$  that does not constrain  $\mu_1$  and a system guarantee  $MT_y \rightarrow M_1$  that constrains at most  $\mu_1$ .

The specification  $S_2$  of the second component in Figure 5 is similar, where  $\mu_2$ ,  $E_2$ ,  $M_2$ , and  $S_2$  are obtained from  $\mu_1$ ,  $E_1$ ,  $M_1$ , and  $S_1$  by substituting 2 for 1,  $x$  for  $y$ , and  $y$  for  $x$ .

We now compose specifications  $S_1$  and  $S_2$ . The definitions of  $M_1$  and  $M_2$  and the observation that  $P \rightarrow Q$  implies  $P \Rightarrow Q$  yield

$$(MT_x \vee MT_y) \wedge (MT_y \rightarrow M_1) \wedge (MT_x \rightarrow M_2) \Rightarrow M_1 \wedge M_2 \quad (13)$$

The definitions of  $M_1$  and  $M_2$  and simple temporal reasoning yield

$$E \wedge M_1 \wedge M_2 \Rightarrow E_1 \wedge E_2 \quad (14)$$

where

$$E \stackrel{\Delta}{=} \square[\mu_1 \vee \mu_2]_{(x,y,now)}$$

Combining (13) and (14) proves

$$E \wedge (MT_x \vee MT_y) \wedge (MT_y \rightarrow M_1) \wedge (MT_x \rightarrow M_2) \Rightarrow E_1 \wedge E_2$$

We can therefore apply Theorem 3, substituting  $E \wedge (MT_x \vee MT_y)$  for  $E$ ,  $MT_y \rightarrow M_1$  for  $M_1$ , and  $MT_x \rightarrow M_2$  for  $M_2$ , to deduce

$$S_1 \wedge S_2 \Rightarrow (E \wedge (MT_x \vee MT_y) \rightarrow (MT_y \rightarrow M_1) \wedge (MT_x \rightarrow M_2))$$

Using the implication-like properties of  $\rightarrow$ , this simplifies to

$$S_1 \wedge S_2 \Rightarrow (E \rightarrow (MT_y \rightarrow M_1) \wedge (MT_x \rightarrow M_2)) \quad (15)$$

All one can conclude about the composition from (15) is: either  $x$  and  $y$  are both 0 when *now* reaches 12, or neither of them is 0 when *now* reaches 12. There is no paradox.

As another example, we replace  $S_2$  by the specification  $E_2 \rightarrow M_2$ . This specification, which we call  $S_3$ , asserts that the system sets  $y$  to 0 by noon, regardless of whether the environment sets  $x$  to 0. The definitions imply

$$MT_y \wedge E \wedge (MT_y \rightarrow M_1) \wedge M_2 \Rightarrow E_1 \wedge E_2$$

and Theorem 3 yields

$$S_1 \wedge S_3 \Rightarrow (E \rightarrow (MT_x \rightarrow M_1) \wedge M_2)$$

Since  $M_2$  implies  $MT_x$ , this simplifies to

$$S_1 \wedge S_3 \Rightarrow (E \rightarrow M_1 \wedge M_2)$$

The composition of  $S_1$  and  $S_3$  does guarantee that both  $x$  and  $y$  equal 0 by noon.

## 5.2 Timing Constraints in General

Our no-longer-paradoxical example suggests that the form of a real-time open system specification should be

$$E \rightarrow (P \rightarrow M) \tag{16}$$

where  $M$  describes the system's timing constraints and the advancing of *now*, and  $P$  describes the upper-bound timing constraints for the environment. Since the environment's lower-bound timing constraints do not constrain the advance of *now*, they can remain in  $E$ . As we observed in Section 4.4, proving that one open specification implements another reduces to the proof for the corresponding closed systems. Since  $E \rightarrow (P \rightarrow M)$  is equivalent to  $(E \wedge P) \rightarrow M$ , the closed system corresponding to (16) is the expected one,  $E \wedge P \wedge M$ .

For the specification (16) to be reasonable, its closed system version,  $E \wedge P \wedge M$ , should be nonZeno. However, this is not sufficient. Consider a specification guaranteeing that the system produces a sequence of outputs until the environment sends a *stop* message, where the  $n^{\text{th}}$  output must occur by time  $(n - 1)/n$ . There is no timing assumption on the environment; it need never send a *stop* message. This is an unreasonable specification because *now* cannot reach 1 until the environment sends its *stop* message, so the advance of time is contingent on an optional action of the environment. However, the corresponding closed system specification is nonZeno, since time can always be made to advance without bound by having the environment send a *stop* message.

If advancing *now* is a  $\mu$  action, then a system that controls  $\mu$  actions can guarantee time to be unbounded while satisfying a safety specification  $S$  iff the pair  $(S, NZ)$  is  $\mu$ -machine realizable. We therefore take this condition to be the definition of nonZenoness for an open system specification  $S$ .

For specifications in terms of  $\delta$ -timers, nonZenoness can be proved with generalizations to open systems of the theorems in Section 3.3. The following is the generalization of the strongest of them, Theorem 2. It is applied to a specification of the form (16) by substituting  $E \wedge P$  for  $E$ .

**Theorem 4** *With the notation and hypotheses of Theorem 2, if  $E$  and  $M$  are safety properties such that  $\Pi = E \wedge M$ , and*

5.  $M$  constrains at most  $\mu$ ,
6. (a)  $\langle \mathcal{A}_k \rangle_v \Rightarrow \mu$ , for all  $k \in I \cup J$ ,  
(b)  $(now' \neq now) \Rightarrow \mu$

then  $(E \rightarrow M', NZ)$  is  $\mu$ -machine realizable, where

$$M' \triangleq M \wedge RT_v \wedge \forall i \in I : \text{MinTime}(t_i, \mathcal{A}_i, v) \wedge \forall j \in J : \text{MaxTime}(T_j)$$

The proof of Theorem 4, which appears in the appendix, is similar to the proof of Theorem 2 sketched in Section 3.3. It uses Propositions 4 and 5 instead of Propositions 1 and 3. Since machine realizability implies machine closure, Theorem 2 follows from Theorem 4 by letting  $E$  and  $\mu$  equal true and  $M$  equal  $\Pi$ .

Theorem 4 applies to the internal specifications, where all variables are visible. For closed systems, existential quantification is handled with Proposition 2. For open systems, the generalization of this proposition—the analog of Proposition 10 of [1]—is needed.

## 6 Conclusion

### 6.1 What We Did

We started with a simple idea—specifying and reasoning about real-time systems by representing time as an ordinary variable. This idea led to an exposition that most readers probably found quite difficult. What happened to the simplicity?

About half of the exposition is a review of concepts unrelated to real time. All the fundamental concepts described in Sections 2 and 4, including machine closure, machine realizability, and the  $\rightarrow$  operator, have appeared before [1, 2]. These concepts are subtle, but they are important for understanding any concurrent system; they were not invented for real-time systems.

We chose to formulate these concepts in TLA. Like any language, TLA seems complicated on first encounter. We believe that a true measure of simplicity of a formal language is the simplicity of its formal description. The complete syntax and formal semantics of TLA are given in about 1-1/2 pages of figures in [13].

We never claimed that specifying and reasoning about concurrent systems is easy. Verifying concurrent systems is difficult and error prone. Our assertions that one formula follows from another, made so casually in the exposition, must be backed up by detailed calculations. We have omitted the proofs for our examples, which, done with the same detail as the proofs in the appendix, occupy some twenty pages.

We did claim that existing methods for specifying and reasoning about concurrent systems could be applied to real-time systems. Now, we can examine how hard they were to apply.

We found few obstacles in the realm of closed systems. The second author has more than fifteen years of experience in the formal verification of concurrent algorithms, and we knew that old-fashioned methods could be applied to real-time systems. However, TLA is relatively new, and we were pleased by how well it worked. The formal specification of Fischer's protocol in Figure 4, obtained by conjoining timing constraints to the untimed protocol, is as simple and direct as we could have hoped for. Moreover, the formal correctness proofs of this protocol and of the queue example, using the method of reasoning described in [13], were straightforward. Perhaps the most profound discovery was the relation between nonZenoness and machine closure.

Open systems made up for any lack of difficulty with closed systems. State-based approaches to open systems were a fairly recent development, and we had little experience with them. Studying real-time systems taught us a great deal, and led to a number of changes from the approach in [1]. For example, we now write specifications with  $\rightarrow$  instead of  $\Rightarrow$ , and we put initial conditions in the system guarantee rather than in the environment assumption. Many alternative ways of writing real-time specifications seemed plausible; choosing one that works was surprisingly hard. Even the simple idea of putting the environment's timing assumptions to the left of a  $\rightarrow$  in the system's guarantee came only after numerous failed efforts. Although the basic ideas we need to handle real-time open systems



seem to be in place, we still have much to learn before reasoning about open systems becomes routine.

## 6.2 Beyond Real Time

Real-time systems introduce a fundamentally new problem: adding physical continuity to discrete systems. Our solution is based on the observation that, when reasoning about a discrete system, we can represent continuous processes by discrete actions. If we can pretend that the system progresses by discrete atomic actions, we can pretend that those actions occur at a single instant of time, and that the continuous change to time also occurs in discrete steps. If there is no system action between noon and  $\sqrt{2}$  seconds past noon, we can pretend that time advances by those  $\sqrt{2}$  seconds in a single action.

Physical continuity arises not just in real-time systems, but in “real-pressure” and “real-temperature” process-control systems. Such systems can be described in the same way as real-time systems: pressure and temperature as well as time are represented by ordinary variables. The continuous changes to pressure and temperature that occur between system actions are represented by discrete changes to the variables. The fundamental assumption is that the real, physical system is accurately represented by a model in which the system makes discrete, instantaneous changes to the physical parameters it affects.

The observation that continuous parameters other than time can be modeled by program variables has probably been known for years. However, the first published work we know of that uses this idea, by Marzullo, Schneider, and Budhiraja [14], appeared only recently.

# Appendix

## A Definitions

### A.1 States and Behaviors

$\Sigma$ : The set of all states (assignments of values to variables).

$\Sigma^*$ : The set of finite behaviors (finite sequences of states).

$\Sigma^\infty$ : The set of behaviors (infinite sequences of states).

$\lambda$ : The empty sequence.

$|\sigma|$  [for  $\sigma \in \Sigma^* \cup \Sigma^\infty$ ]: The length of  $\sigma$ .

$\sigma_n$  [for  $\sigma \in \Sigma^* \cup \Sigma^\infty$  and  $0 < n \leq |\sigma|$ ]: The  $n^{\text{th}}$  state in the sequence  $\sigma$ .

$\sigma|_n$  [for  $\sigma \in \Sigma^* \cup \Sigma^\infty$  and  $0 \leq n \leq |\sigma|$ ]: The finite behavior  $\sigma_1, \dots, \sigma_n$ ; equal to  $\lambda$  when  $n = 0$ .

$\sigma \circ s$  [for  $\sigma \in \Sigma^*$  and  $s \in \Sigma$ ]: The finite behavior  $\sigma_1, \dots, \sigma_{|\sigma|}, s$ .

$\sigma \simeq \tau$  [for  $\sigma, \tau \in \Sigma^*$  or  $\sigma, \tau \in \Sigma^\infty$ ]:  $\sigma$  and  $\tau$  are equal except for stuttering steps (repetitions of identical states).

$\sigma \simeq_x \tau$  [for  $x$  a tuple of variables and either  $\sigma, \tau \in \Sigma^*$  or  $\sigma, \tau \in \Sigma^\infty$ ]:

There exist behaviors  $\hat{\sigma}$  and  $\hat{\tau}$  such that

$\wedge \hat{\sigma} \simeq \sigma$  and  $\hat{\tau} \simeq \tau$

$\wedge |\hat{\sigma}| = |\hat{\tau}|$

$\wedge$  for every integer  $n \leq |\hat{\sigma}|$ , the states  $\hat{\sigma}_n$  and  $\hat{\tau}_n$  are equal except for the values they assign to the variables of  $x$ .

### A.2 Predicates and Actions

*predicate*: A subset of  $\Sigma$ .

*state function*: A mapping from  $\Sigma$  to the set of values.

*action*: A subset of  $\Sigma \times \Sigma$ .

*transition function*: A mapping from  $\Sigma \times \Sigma$  to the set of values.

$P'$  [for  $P$  a predicate]: The action  $\{(s, t) : t \in P\}$ .

$f'$  [for  $f$  a state function]: The transition function such that  $f'(s, t) = f(t)$ .

*Enabled*  $\mathcal{A}$  [for  $\mathcal{A}$  an action]: The predicate  $\{s \in \Sigma : (\exists t \in \Sigma : (s, t) \in \mathcal{A})\}$ .

$[\mathcal{A}]_f$  [for  $\mathcal{A}$  an action and  $f$  a state function]:  $\mathcal{A} \vee (f' = f)$

$\langle \mathcal{A} \rangle_f$  [for  $\mathcal{A}$  an action and  $f$  a state function]:  $\neg[\neg\mathcal{A}]_f$  (equals  $\mathcal{A} \wedge (f' \neq f)$ ).

### A.3 Properties

*property*: A subset  $\Pi$  of  $\Sigma^\infty$  such that for any  $\sigma, \tau$  in  $\Sigma^\infty$ , if  $\sigma \simeq \tau$  then  $\sigma \in \Pi$  iff  $\tau \in \Pi$ .

$\Box\Pi$  [for  $\Pi$  a property]:

The property  $\{\sigma \in \Sigma^\infty : (\forall n > 0 : \sigma_n, \sigma_{n+1}, \dots \in \Pi)\}$ .

$\Box[\mathcal{A}]_f$  [for  $\mathcal{A}$  an action and  $f$  a state function]:

The property  $\{\sigma \in \Sigma^\infty : (\forall n > 0 : (\sigma_n, \sigma_{n+1}) \in [\mathcal{A}]_f)\}$ .

$\Diamond\Pi$  [for  $\Pi$  a property or an action of the form  $\langle \mathcal{A} \rangle_f$ ]:  $\neg\Box\neg\Pi$

$\Pi \rightsquigarrow \Phi$  [for  $\Pi$  and  $\Phi$  properties]:  $\Box(\Pi \Rightarrow \Diamond\Phi)$

$WF_f(\mathcal{A})$  [for  $f$  a state function and  $\mathcal{A}$  an action]:

The property  $(\Box\Diamond\langle \mathcal{A} \rangle_f) \vee (\Box\Diamond\neg\text{Enabled}\langle \mathcal{A} \rangle_f)$ .

$SF_f(\mathcal{A})$  [for  $f$  a state function and  $\mathcal{A}$  an action]:

The property  $(\Box\Diamond\langle \mathcal{A} \rangle_f) \vee (\Diamond\Box\neg\text{Enabled}\langle \mathcal{A} \rangle_f)$ .

$\exists x : \Pi$  [for  $x$  a tuple of variables and  $\Pi$  a property]:

The property  $\{\sigma \in \Sigma^\infty : (\exists \rho \in \Pi : \rho \simeq_x \sigma)\}$ .

### A.4 Closure and Safety

$\sigma \models \Pi$  [for  $\sigma \in \Sigma^*$  and  $\Pi$  a property]:

There exists  $\tau \in \Pi$  and  $n$  such that  $\sigma = \tau|_n$ .

$\mathcal{C}(\Pi)$  [for  $\Pi$  a property]: The property  $\{\sigma \in \Sigma^\infty : (\forall n \geq 0 : \sigma|_n \models \Pi)\}$ .

*safety property*: A property  $\Pi$  such that  $\Pi = \mathcal{C}(\Pi)$ .

$(\Pi, L)$  *machine closed* [for  $\Pi$  and  $L$  properties]:  $\Pi = \mathcal{C}(\Pi \wedge L)$

$\Phi \rightarrow \Pi$  [for  $\Phi$  and  $\Pi$  properties]: The property consisting of all behaviors  $\sigma$  such that

$\wedge \sigma \in \Phi$  implies  $\sigma \in \Pi$

$\wedge$  for all  $n \geq 0$ :  $\sigma|_n \models \Phi$  implies  $\sigma|_n \models \Pi$ .

### A.5 Realizability

$\Pi$  *constrains at most*  $\mu$  [for  $\Pi$  a safety property and  $\mu$  an action]:

For any  $s \in \Sigma$  and  $\rho \in \Sigma^*$  with  $|\rho| > 0$ , if  $\rho \models \Pi$  and  $(\rho|_{|\rho|}, s) \notin \mu$ , then  $\rho \circ s \models \Pi$ .

$\Pi$  *does not constrain*  $\mu$  [for  $\Pi$  a safety property and  $\mu$  an action]:

$\Pi$  constrains at most  $\neg\mu$ , and every behavior of length 1 satisfies  $\Pi$ .

$\mu$ -*strategy* [for  $\mu$  an action]: A mapping  $f$  from  $\Sigma^*$  to  $\Sigma \cup \{\perp\}$  such that  $f(\rho) \neq \perp$  implies  $(\rho|_{|\rho|}, f(\rho)) \in \mu$ , for any sequence  $\rho \in \Sigma^*$  with  $\rho \neq \lambda$ .

$\mathcal{O}_\mu(f)$  [for  $\mu$  an action and  $f$  a  $\mu$ -strategy]: The set of behaviors  $\sigma$  such that

$\wedge f(\lambda) = \sigma_1$   
 $\wedge$  for all  $n > 0$ :  $(\sigma_n, \sigma_{n+1}) \in \mu$  implies  $\sigma_{n+1} = f(\sigma|_n)$   
 $\wedge$  for infinitely many  $n$ :  $f(\sigma|_n) = \perp$  or  $(\sigma_n, \sigma_{n+1}) \in \mu$ .  
 $\mathcal{O}_\mu(f, \rho)$  [for  $\mu$  an action,  $f$  a  $\mu$ -strategy, and  $\rho \in \Sigma^*$ ]: If  $\rho = \lambda$  then  $\mathcal{O}_\mu(f)$ , else the set of behaviors  $\sigma$  such that  
 $\wedge \tau = \sigma|_{|\tau|}$   
 $\wedge$  for all  $n \geq |\tau|$ :  $(\sigma_n, \sigma_{n+1}) \in \mu$  implies  $\sigma_{n+1} = f(\sigma|_n)$   
 $\wedge$  for infinitely many  $n$ :  $f(\sigma|_n) = \perp$  or  $(\sigma_n, \sigma_{n+1}) \in \mu$ .  
 $\Pi$  is  $\mu$ -receptive [for  $\Pi$  a property and  $\mu$  an action]: For every behavior  $\sigma \in \Pi$  there exists a  $\mu$ -strategy  $f$  such that  $\sigma \in \mathcal{O}_\mu(f)$  and  $\mathcal{O}_\mu(f) \subseteq \Pi$ .  
 $(\Pi, L)$  is  $\mu$ -machine realizable [for  $\Pi$  and  $L$  properties and  $\mu$  an action]:  
 $(\Pi, L)$  is machine closed and  $\Pi \wedge L$  is  $\mu$ -receptive.

## B Proofs

### B.1 Proof Notation

We use hierarchically structured proofs. The theorem to be proved is statement  $\langle 0 \rangle 1$ . The proof of statement  $\langle i \rangle j$  is either an ordinary paragraph-style proof or the sequence of statements  $\langle i + 1 \rangle 1, \langle i + 1 \rangle 2, \dots$  and their proofs. A proof may be preceded by a proof sketch, which informally describes the proof. Within a proof,  $\langle k \rangle l$  denotes the most recent statement with that number. A statement has the form

ASSUME: *Assump* PROVE: *Goal*

which is abbreviated to *Goal* if there is no assumption. The assertion Q.E.D. in statement number  $\langle i + 1 \rangle k$  of the proof of statement  $\langle i \rangle j$  denotes the goal of statement  $\langle i \rangle j$ . The statement

CASE: *Assump*

is an abbreviation for

ASSUME: *Assump* PROVE: Q.E.D.

Within the proof of statement  $\langle i \rangle j$ , assumption  $\langle i \rangle$  denotes that statement's assumption, and  $\langle i \rangle.k$  denotes the assumption's  $k^{\text{th}}$  item.

We recommend that proofs be read hierarchically, from the top level down. To read the proof of a long level- $k$  step: (i) read the level- $(k + 1)$  statements that comprise its proof, together with the proof of the final Q.E.D. step (which is usually a short paragraph), (ii) read the proof of each level- $(k + 1)$  step, in any desired order.

## B.2 Proof of Proposition 2

ASSUME: 1.  $(\Pi, L)$  is machine-closed.

2.  $x$  a tuple of variables, none of which occurs free in  $L$ .

3.  $\rho$  is a finite behavior satisfying  $\exists x : \Pi$ .

PROVE: There exists an infinite behavior  $\tau$  satisfying  $(\exists x : \Pi) \wedge L$  such that  $\rho$  is a prefix of  $\tau$ .

(1)1. Choose a finite behavior  $\sigma$  satisfying  $\Pi$  such that  $\sigma \simeq_x \rho$ .

(2)1. Choose a behavior  $\phi$  such that  $\phi \in \exists x : \Pi$  and  $\rho$  a prefix of  $\phi$ .

PROOF: Assumption (0).1.

(2)2. Choose a behavior  $\psi$  such that  $\psi \in \Pi$  and  $\psi \simeq_x \phi$ .

PROOF: (2)1 and the definition of  $\exists x : \Pi$ .

(2)3. There exists an initial prefix  $\sigma$  of  $\psi$  such that  $\sigma \simeq_x \rho$ .

PROOF:  $\psi \simeq_x \phi$  (by (2)2) and  $\rho$  a prefix of  $\phi$  (by (2)1).

(2)4. Q.E.D.

PROOF: (2)2 and (2)3.

(1)2. Choose  $\eta$  satisfying  $\Pi \wedge L$  such that  $\sigma$  is a prefix of  $\eta$ .

PROOF: The existence of  $\tau$  follows from (1)1 and assumption (0).1.

(1)3. Choose  $\tau$  such that  $\tau \simeq_x \eta$  and  $\rho$  is a prefix of  $\tau$ .

PROOF:  $\tau$  exists because  $\sigma$  is a prefix of  $\eta$  (by (1)2) and  $\sigma \simeq_x \rho$  (by (1)1).

(1)4.  $\tau$  satisfies  $\exists x : \Pi$ .

PROOF: (1)2, (1)3, and the definition of  $\exists x : \Pi$ .

(1)5.  $\tau$  satisfies  $L$ .

PROOF: (1)2, (1)3, and assumption (0).2.

(1)6. Q.E.D.

PROOF: Steps (1)4 and (1)5 imply that  $\tau$  satisfies  $(\exists x : \Pi) \wedge L$ , and  $\rho$  is a prefix of  $\tau$  by (1)3.

## B.3 Proof of Proposition 3

ASSUME: 1.  $(\Pi, L_1)$  is machine closed.

2.  $\Pi \wedge L_1$  implies  $L_2$ .

PROVE:  $(\Pi, L_2)$  is machine closed.

PROOF:  $\Pi = \mathcal{C}(\Pi \wedge L_1)$  [(0).1]

$\subseteq \mathcal{C}(\Pi \wedge L_2)$  [(0).2 and monotonicity of closure]

$\subseteq \mathcal{C}(\Pi)$  [monotonicity of closure]

$= \Pi$  [(0).1, which implies  $\Pi$  closed]

This proves that  $\Pi = \mathcal{C}(\Pi \wedge L_2)$ .  $\square$

## B.4 Two Lemmas About Machine-Realizability

**Lemma 1**  $(\Pi, L)$  is  $\mu$ -machine realizable iff for every finite behavior  $\tau$  satisfying  $\Pi$ , there exists a  $\mu$ -strategy  $f$  such that  $\tau \models \mathcal{O}_\mu(f)$  and  $\mathcal{O}_\mu(f) \subseteq \Pi \wedge L$ .

PROOF: This is proved in Proposition 9 of [1].  $\square$

**Lemma 2**  $(\Pi, L)$  is  $\mu$ -machine realizable iff for every finite behavior  $\tau$  satisfying  $\Pi$ , there exists a  $\mu$ -strategy  $f$  such that  $\mathcal{O}_\mu(f, \tau) \subseteq \Pi \wedge L$ .

- (1)1. ASSUME: 1. For all finite  $\tau$  such that  $\tau \models \Pi$ , there exists  $f_\tau$  such that
- a.  $f_\tau$  is a  $\mu$ -strategy.
  - b.  $\mathcal{O}_\mu(f_\tau, \tau) \subseteq \Pi \wedge L$
2.  $\rho$  a finite behavior such that  $\rho \models \Pi$ .

PROVE: There exists a  $\mu$ -strategy  $g$  such that

$$\wedge \rho \models \mathcal{O}_\mu(g)$$

$$\wedge \mathcal{O}_\mu(g) \subseteq \Pi \wedge L$$

- (2)1. Choose  $g$  such that, for all  $\sigma \in \Sigma^*$ :

$$g(\sigma) \triangleq \begin{array}{l} \mathbf{if} (\sigma = \rho|_n) \wedge (n < |\rho|) \\ \quad \mathbf{then if} (n = 0) \vee (\rho_n, \rho_{n+1}) \in \mu \\ \quad \quad \mathbf{then} \rho_{n+1} \\ \quad \quad \mathbf{else} \perp \\ \quad \mathbf{else} f_\eta(\sigma), \text{ where } \eta \text{ is the longest common} \\ \quad \quad \text{prefix of } \rho \text{ and } \sigma \end{array}$$

PROOF: The requisite  $f_\eta$  exists by assumption (0).2, since  $\rho \models \Pi$  implies  $\eta \models \Pi$ , for any prefix  $\eta$  of  $\rho$ .

- (2)2.  $g$  is a  $\mu$ -strategy.

PROOF: (0).1a and the definition of  $g$  ((2)1).

- (2)3.  $\mathcal{O}_\mu(g) \subseteq \Pi \wedge L$

PROOF: Assumption (0).1b, since  $\kappa \in \mathcal{O}_\mu(g)$  implies that  $\kappa \in \mathcal{O}_\mu(f_\eta, \eta)$ , where  $\eta$  is the longest common prefix of  $\kappa$  and  $\rho$ .

- (2)4.  $\rho \models \mathcal{O}_\mu(g)$

PROOF: The definition of  $g$  implies that  $\rho$  is a prefix of an element of  $\mathcal{O}_\mu(g)$ .

- (2)5. Q.E.D.

PROOF: (2)2, (2)3, and (2)4.

- (1)2. ASSUME:  $(\Pi, L)$  is  $\mu$ -machine realizable and  $\tau \models \Pi$ .

PROVE: There exists a  $\mu$ -strategy  $f$  such that  $\mathcal{O}_\mu(f, \tau) \subseteq \Pi \wedge L$ .

- (2)1. Choose a  $\mu$ -strategy  $f$  such that  $\tau \in \mathcal{O}_\mu(f)$  and  $\mathcal{O}_\mu(f) \subseteq \Pi \wedge L$ .

PROOF: Assumption (2) and Lemma 1.

(2)2.  $\mathcal{O}_\mu(f, \tau) \subseteq \Pi \wedge L$

PROOF: (2)1.2 and the definition of  $\mathcal{O}_\mu(f, \tau)$ .

(2)3. Q.E.D.

PROOF: (2)1 and (2)2.

(1)3. Q.E.D.

PROOF: By (1)1, (1)2 and Lemma 1.

## B.5 Proof of Proposition 4

LET:  $\Psi \triangleq \Phi \wedge \Pi$

$L \triangleq \bigwedge \forall i \in I : \text{WF}_{w_i}(\mathcal{A}_i)$   
 $\bigwedge \forall j \in J : \text{SF}_{w_j}(\mathcal{A}_j)$

ASSUME: 1.  $\Phi$  and  $\Pi$  are safety properties.

2.  $\Pi$  constrains at most  $\mu$ .

3.  $I \cup J$  a finite or countably infinite set, which we take to be a set of natural numbers.

4. For all  $k$  in  $I \cup J$ :

a.  $\langle \mathcal{A}_k \rangle_{w_k}$  is a subaction of  $\Psi$ .

b.  $\Psi \Rightarrow \Box[\langle \mathcal{A}_k \rangle_{w_k} \Rightarrow \mu]_{w_k}$

5. For all  $j$  in  $J$ : *Enabled*  $\langle \mathcal{A}_j \rangle_{w_j}$  is a  $\neg\mu$  invariant of  $\Psi$ .

PROVE:  $(\Phi \rightarrow \Pi, \Phi \Rightarrow L)$  is  $\mu$ -machine realizable.

By Lemma 2, it suffices to

ASSUME: 6.  $\tau \models \Phi \rightarrow \Pi$

PROVE: There exists a  $\mu$ -strategy  $f$  such that

$$\mathcal{O}_\mu(f, \tau) \subseteq (\Phi \rightarrow \Pi) \wedge (\Phi \Rightarrow L).$$

PROOF SKETCH: Defining the strategy  $f$  is tantamount to constructing a scheduler that executes the actions  $\langle \mathcal{A}_k \rangle_{w_k}$  to satisfy the fairness requirements. Action  $\langle \mathcal{A}_k \rangle_{w_k}$  is never considered until  $k$  steps have occurred, so only finitely many actions are considered at any point. The next action scheduled is the enabled action that has been under consideration for the longest time without having been executed. In the event of ties, the lowest-numbered action is chosen. The strategy  $f$  will never violate  $\Psi$ , and it stops making moves if  $\Psi$  is ever made false by the environment. In the following definitions,  $nextact(\rho)$  is the  $k$  such that the scheduler chooses to execute  $\langle \mathcal{A}_k \rangle_{w_k}$  after the finite behavior  $\rho$ .

LET:  $\mathcal{B}_k \triangleq \langle \mathcal{A}_k \rangle_{w_k}$

$n \triangleq |\tau|$

$lasttime(\rho, k) \triangleq \max(\{l : 1 < l \leq |\rho| : (\rho_{l-1}, \rho_l) \in \mathcal{B}_k\} \cup \{k\})$

$able(\rho) \triangleq \text{if } \rho = \lambda \text{ then the set of naturals}$   
 $\qquad \qquad \qquad \text{else } \{k : \rho_{|\rho|} \in Enabled \mathcal{B}_k\}$   
 $oldestlast(\rho) \triangleq \min\{lasttime(\rho, k) : k \in able(\rho)\}$   
 $nextact(\rho) \triangleq \min\{k \in able(\rho) : lasttime(\rho, k) = oldestlast(\rho)\}$

(1)1. Choose  $f$  such that for all  $\rho$  in  $\Sigma^*$ :

**if**  $(able(\rho) = \emptyset) \vee (\rho \notin \Psi)$   
**then**  $f(\rho) = \perp$   
**else**  $\wedge (\rho \neq \lambda) \Rightarrow (\rho_{|\rho|}, f(\rho)) \in \mathcal{B}_{nextact(\rho)}$   
 $\wedge \rho \circ f(\rho) \in \Psi$

PROOF SKETCH: We must show that, for any  $\rho$ , the requisite  $f(\rho)$  exists.

(2)1. ASSUME:  $(able(\rho) \neq \emptyset) \wedge \rho \models \Psi$

PROVE:  $\exists s \in \Sigma : \wedge (\rho \neq \lambda) \Rightarrow (\rho_{|\rho|}, s) \in \mathcal{B}_{nextact(\rho)}$   
 $\wedge \rho \circ s \in \Psi$

(3)1. CASE:  $\rho = \lambda$

PROOF: Assumption (2) and the definition of  $\rho \models \Psi$ .

(4)1. CASE:  $\rho \neq \lambda$

(5)1.  $\rho_{|\rho|} \in Enabled \mathcal{B}_{nextact(\rho)}$

(6)1.  $nextact(\rho) \in able(\rho)$

PROOF: Assumption (2) and the definitions of  $nextact$  and  $oldestlast$ .

(6)2. Q.E.D.

PROOF: Case assumption (4) and the definition of  $able(\rho)$ .

(5)2. Q.E.D.

PROOF: (5)1 and Assumption (0).4a.

(4)2. Q.E.D.

PROOF: (3)1 and (4)1.

(2)2. Q.E.D.

PROOF: The existence of the required  $f$  follows easily from (2)1.

(1)2.  $f$  is a  $\mu$ -strategy.

ASSUME:  $(\rho \neq \lambda) \wedge (f(\rho) \neq \perp)$

PROVE:  $(\rho_{|\rho|}, f(\rho)) \in \mu$

PROOF: (1)1 and assumption (1) imply

$\wedge (\rho_{|\rho|}, f(\rho)) \in \mathcal{B}_{nextact(\rho)}$   
 $\wedge \rho \circ f(\rho) \models \Psi$

and the result follows from (0).4b.

(1)3.  $\mathcal{O}_\mu(f, \tau) \subseteq (\Phi \rightarrow \Pi)$

PROOF SKETCH: This is a straightforward induction proof; (2)1 is the base case and (2)2 is the induction step.

(2)1. ASSUME:  $\sigma \in \mathcal{O}_\mu(f, \tau)$



PROVE:  $\sigma|_0 \models \Phi \rightarrow \Pi$   
PROOF:  $\sigma|_0 = \tau|_0$ , and  $\tau \models (\Phi \rightarrow \Pi)$  by assumption  $\langle 2 \rangle$  and the definition of  $\mathcal{O}_\mu(f, \tau)$ .

$\langle 2 \rangle 2$ . ASSUME: 1.  $\sigma \in \mathcal{O}_\mu(f, \tau)$   
2.  $\sigma|_i \models \Phi \rightarrow \Pi$   
3.  $i \geq 0$

PROVE:  $\sigma|_{i+1} \models \Phi \rightarrow \Pi$

$\langle 3 \rangle 1$ . CASE:  $i \geq |\tau|$   
 $\langle 4 \rangle 1$ . CASE:  $(\sigma_i, \sigma_{i+1}) \notin \mu$   
PROOF: Follows easily from assumption  $\langle 2 \rangle 2$  and assumption  $\langle 0 \rangle 2$ , since  $\Pi$  constrains at most  $\mu$  implies  $\Phi \rightarrow \Pi$  constrains at most  $\mu$ .

$\langle 4 \rangle 2$ . CASE:  $(\sigma_i, \sigma_{i+1}) \in \mu$   
 $\langle 5 \rangle 1$ .  $\sigma_{i+1} = f(\sigma|_i)$   
PROOF: Assumption  $\langle 2 \rangle 1$ .  
 $\langle 5 \rangle 2$ . Q.E.D.  
PROOF:  $\langle 5 \rangle 1$ , assumption  $\langle 2 \rangle 2$ , and the **else** clause in  $\langle 1 \rangle 1$  (the definition of  $f$ ).

$\langle 4 \rangle 3$ . Q.E.D.  
PROOF:  $\langle 4 \rangle 1$  and  $\langle 4 \rangle 2$ .

$\langle 3 \rangle 2$ . CASE:  $i < |\tau|$   
PROOF: By assumption  $\langle 0 \rangle 6$  and assumption  $\langle 2 \rangle 1$ , since  $i < |\tau|$  implies  $\sigma|_{i+1} = \tau|_{i+1}$ .

$\langle 3 \rangle 3$ . Q.E.D.  
PROOF:  $\langle 3 \rangle 1$  and  $\langle 3 \rangle 2$ .

$\langle 2 \rangle 3$ . Q.E.D.  
PROOF:  $\langle 2 \rangle 1$ ,  $\langle 2 \rangle 2$ , mathematical induction, and assumption  $\langle 0 \rangle 1$ .

$\langle 1 \rangle 4$ .  $\mathcal{O}_\mu(f, \tau) \subseteq (\Phi \Rightarrow L)$   
PROOF SKETCH: The proof is by contradiction. We define  $IE$  and  $IO$  to be the sets of indices of actions that should be executed infinitely often and that actually are executed infinitely often, respectively. We show that the existence of a  $q$  in  $IE$  but not in  $IO$  leads to a contradiction.

ASSUME: 1.  $\sigma \in \mathcal{O}_\mu(f, \tau)$   
2.  $\sigma \in \Phi$   
3.  $\exists k \in I \cup J : \text{a. } \forall k \in I \wedge \sigma \notin \text{WF}_{w_k}(\mathcal{A}_k)$   
**b. }  $\forall k \in J \wedge \sigma \notin \text{SF}_{w_k}(\mathcal{A}_k)$**

PROVE: **flase**

LET:  $IE \triangleq \{i \in I : \diamond \square \text{Enabled } \mathcal{B}_i\} \cup \{j \in J : \square \diamond \text{Enabled } \mathcal{B}_j\}$   
 $IO \triangleq \{k \in I \cup J : (\sigma_n, \sigma_{n+1}) \in \mathcal{B}_k \text{ for infinitely many } n\}$

(2)1.  $\sigma \in \Phi \wedge \Pi$

PROOF: (1)3 and assumption (1).1, which imply  $\sigma \in \Phi \rightarrow \Pi$ , and (1).2.

(2)2. Choose  $q$  such that

1.  $q \in IE$
2.  $q \notin IO$

PROOF: Assumption (1).3.

(2)3. Choose  $m_1$  such that

1.  $m_1 > q$
2.  $\forall n \geq m_1 : (\sigma_{n-1}, \sigma_n) \notin \mathcal{B}_q$
3.  $m_1 > |\tau|$

PROOF: (2)2.2

(2)4. Choose  $m_2$  such that

1.  $m_2 > m_1$
2.  $\forall k \in I \cup J : (k \notin IO) \wedge (k < m_1) \Rightarrow \forall n > m_2 : (\sigma_n, \sigma_{n+1}) \notin \mathcal{B}_k$

PROOF: Definition of  $IO$ .

(2)5. Choose  $m_3$  such that

1.  $m_3 > m_2$
2.  $\forall k \in I \cup J : (k \in IO) \wedge (k < m_1) \Rightarrow$   
 $\exists n > m_2 : (n < m_3) \wedge (\sigma_n, \sigma_{n+1}) \in \mathcal{B}_k$

PROOF: Definition of  $IO$ .

(2)6. Choose  $m_4$  such that

1.  $m_4 > m_3$
2.  $\sigma_{m_4} \in \text{Enabled } \mathcal{B}_q$
3.  $(q \in I) \Rightarrow \forall n > m_4 : \sigma_n \in \text{Enabled } \mathcal{B}_q$

PROOF: (2)2.1 and the definition of  $IE$ .

(2)7.  $\forall n > m_4 : 1. \wedge \sigma_n \in \text{Enabled } \mathcal{B}_q$   
2.  $\wedge (\sigma_{n-1}, \sigma_n) \notin \mu$

PROOF SKETCH: By time  $m_4$ ,  $\mathcal{B}_q$  is the next action scheduled for execution. Since  $\mathcal{B}_q$  is never again executed (by (2)3), there can be no further  $\mu$  steps. Since only a  $\mu$  step can disable  $\mathcal{B}_q$  (by assumption (0).5),  $\mathcal{B}_q$  must be enabled forever. The formal proof is by induction on  $n$ ; (3)1 is the base case and (3)2 is the induction step.

(3)1.  $\sigma_{m_4} \in \text{Enabled } \mathcal{B}_q$

PROOF: (2)6.

(3)2. ASSUME: 1.  $\sigma_n \in \text{Enabled } \mathcal{B}_q$   
2.  $(\sigma_{n-1}, \sigma_n) \notin \mu$   
3.  $n \geq m_4$

PROVE:  $\wedge \sigma_{n+1} \in \text{Enabled } \mathcal{B}_q$   
 $\wedge (\sigma_n, \sigma_{n+1}) \notin \mu$

⟨4⟩1.  $(\sigma_n, \sigma_{n+1}) \notin \mu$   
 ASSUME:  $(\sigma_n, \sigma_{n+1}) \in \mu$   
 PROVE: **flase**

⟨5⟩1.  $\vee nextact(\sigma|_n) = q$   
 $\vee \exists k \in I \cup J : \wedge nextact(\sigma|_n) = k$   
 $\wedge k < m_1 \wedge k \notin IO$   
 $\wedge k \in able(\sigma|_n)$

⟨6⟩1.  $q \in able(\sigma|_n)$   
 Assumption ⟨3⟩.1.

⟨6⟩2. ASSUME:  $k \geq m_1$   
 PROVE:  $lasttime(\sigma|_n, k) > lasttime(\sigma|_n, q)$   
 PROOF:  $lasttime(\sigma|_n, k) \geq m_1$  by definition of  $lasttime$ , and  $m_1 > lasttime(\sigma|_n, q)$  by assumption ⟨3⟩.1.

⟨6⟩3. ASSUME:  $(k > m_1) \wedge (k \in IO)$   
 PROVE:  $lasttime(\sigma|_n, k) > lasttime(\sigma|_n, q)$   
 PROOF:  $lasttime(\sigma|_n, k) \geq m_2 > m_1$  by ⟨2⟩5, and  $m_1 > lasttime(\sigma|_n, q)$  by assumption ⟨3⟩.1.

⟨6⟩4.  $\vee oldestlast(\sigma|_n) = q$   
 $\vee \exists k \in I \cup J : \wedge oldestlast(\sigma|_n) = k$   
 $\wedge k < m_1 \wedge k \notin IO$   
 $\wedge k \in able(\sigma|_n)$   
 PROOF: ⟨6⟩1, ⟨6⟩2, and ⟨6⟩3.

⟨6⟩5. Q.E.D.  
 PROOF: ⟨6⟩4 and definition of  $nextact$ .

⟨5⟩2.  $\vee (\sigma_n, \sigma_{n+1}) \in \mathcal{B}_q$   
 $\vee \exists k \in I \cup J : \wedge (\sigma_n, \sigma_{n+1}) \in \mathcal{B}_k$   
 $\wedge k < m_1 \wedge k \notin IO$   
 PROOF: ⟨5⟩1, the definition of  $f$  (⟨1⟩1), assumption ⟨4⟩, and assumption ⟨1⟩.1, since  $n > |\tau|$  by assumption ⟨3⟩.3 and ⟨2⟩3–⟨2⟩6.

⟨5⟩3. Q.E.D.

⟨6⟩1. CASE:  $(\sigma_n, \sigma_{n+1}) \in \mathcal{B}_q$   
 PROOF: Contradicts ⟨2⟩3, since  $n > m_1$  by assumption ⟨3⟩.3 and ⟨2⟩4–⟨2⟩6.

⟨6⟩2. CASE:  $\exists k \in I \cup J : \wedge (\sigma_n, \sigma_{n+1}) \in \mathcal{B}_k$   
 $\wedge k < m_1 \wedge k \notin IO$   
 PROOF: Contradicts ⟨2⟩4, since  $n > m_2$  by assumption ⟨3⟩.3, ⟨2⟩5, and ⟨2⟩6.

⟨6⟩3. Q.E.D.  
 PROOF: ⟨6⟩1 and ⟨6⟩2.

⟨4⟩2.  $\sigma_{n+1} \in \text{Enabled } \mathcal{B}_q$   
 PROOF: Assumption ⟨3⟩.1, ⟨4⟩1, ⟨2⟩1, and assumption ⟨0⟩.5.  
 ⟨4⟩3. Q.E.D.  
 PROOF: ⟨4⟩1 and ⟨4⟩2.  
 ⟨3⟩3. Q.E.D.  
 PROOF: ⟨3⟩1, ⟨3⟩2, and mathematical induction.  
 ⟨2⟩8. Q.E.D.  
 ⟨3⟩1.  $\forall n > m_4 : f(\sigma|_n) \neq \perp$   
 PROOF: ⟨2⟩7.1, assumption ⟨1⟩.1, assumption ⟨1⟩.2, and ⟨1⟩3.  
 ⟨3⟩2. Q.E.D.  
 PROOF: ⟨3⟩1 and ⟨2⟩7.2 contradict assumption ⟨1⟩.1.  
 ⟨1⟩5. Q.E.D.  
 PROOF: ⟨1⟩2, ⟨1⟩3, and ⟨1⟩4.

## B.6 Proof of Proposition 5

ASSUME: 1.  $\Phi$  and  $\Pi$  are safety properties.

2.  $(\Phi \rightarrow \Pi, L_1)$  is  $\mu$ -machine realizable.

3.  $(\text{true}, L_2)$  is  $\mu$ -machine realizable.

4.  $\Phi \wedge \Pi \wedge L_1$  implies  $L_2$ .

PROVE:  $(\Phi \rightarrow \Pi, L_2)$  is  $\mu$ -machine realizable.

⟨1⟩1. ASSUME:  $\rho$  is a finite behavior such that  $\rho \models (\Phi \rightarrow \Pi)$ .

PROVE: There exists a  $\mu$ -strategy  $f$  such that

$$\mathcal{O}_\mu(f, \rho) \subseteq (\Phi \rightarrow \Pi) \wedge L_2.$$

⟨2⟩1. Choose  $\mu$ -strategy  $h$  such that  $\mathcal{O}_\mu(h, \rho) \subseteq (\Phi \rightarrow \Pi) \wedge L_1$ .

PROOF: Assumption ⟨0⟩.2, assumption ⟨1⟩, and Lemma 2.

⟨2⟩2. For all  $\tau$ , choose a  $\mu$ -strategy  $g_\tau$  such that  $\mathcal{O}_\mu(g_\tau, \tau) \subseteq L_2$ .

PROOF: Assumption ⟨0⟩.3 and Lemma 2.

⟨2⟩3. Let  $f(\tau) = h(\tau)$  for  $\tau \models \Phi$ , and otherwise let  $f(\tau) = g_\eta(\tau)$  where  $\eta$  is the shortest prefix of  $\tau$  such that  $\eta \not\models \Phi$ . Then  $f$  is a  $\mu$ -strategy.

PROOF: By steps ⟨2⟩1 and ⟨2⟩2 (which say that  $h$  and  $g_\eta$  are  $\mu$ -strategies).

⟨2⟩4.  $\mathcal{O}_\mu(f, \rho) \subseteq (\Phi \rightarrow \Pi) \wedge L_2$

ASSUME:  $\tau \in \mathcal{O}_\mu(f, \rho)$

PROVE:  $\tau \in (\Phi \rightarrow \Pi) \wedge L_2$

⟨3⟩1. CASE:  $\tau \in \Phi$

⟨4⟩1.  $\tau \in \mathcal{O}_\mu(h, \rho)$

PROOF: By assumption ⟨2⟩, since the case assumption ⟨3⟩ and the definition of  $f$  (step ⟨2⟩3) imply  $f(\tau|_n) = h(\tau|_n)$  for all  $n$ .

⟨4⟩2.  $\tau \in (\Phi \rightarrow \Pi) \wedge L_1$

PROOF: By step ⟨4⟩1 and the choice of  $h$  (step ⟨2⟩1).

⟨4⟩3.  $\tau \in \Phi \wedge \Pi \wedge L_1$

PROOF: By step ⟨4⟩2 and case assumption ⟨3⟩.

⟨4⟩4. Q.E.D.

PROOF: By step ⟨4⟩3 and assumption ⟨0⟩.4.

⟨3⟩2. CASE:  $\tau \notin \Phi$

⟨4⟩1. Let  $n$  be the least integer such that  $\tau|_n \notin \Phi$ .

PROOF: Such an  $n$  exists by case assumption ⟨3⟩ and assumption ⟨0⟩.1.

⟨4⟩2.  $\tau \in \mathcal{O}_\mu(g_{\tau|_n}, \tau|_n)$

PROOF: By step ⟨4⟩1, assumption ⟨2⟩, and the definition of  $f$  (step ⟨2⟩3).

⟨4⟩3.  $\tau \in L_2$

PROOF: By steps ⟨4⟩2 and the choice of  $g_{\tau|_n}$  (step ⟨2⟩2).

⟨4⟩4. Q.E.D.

PROOF: By steps ⟨4⟩3 and case assumption ⟨3⟩.

⟨3⟩3. Q.E.D.

PROOF: By steps ⟨3⟩1 and ⟨3⟩2.

⟨2⟩5. Q.E.D.

PROOF: By steps ⟨2⟩3 and ⟨2⟩4.

⟨1⟩2. Q.E.D.

PROOF: The result follows immediately from step ⟨1⟩1 and Lemma 2.

## B.7 Proof of Theorem 4

The proof of Theorem 4 is rather long and is presented in two steps. Section B.7.1 contains a high-level view of the proof; Section B.7.2 contains the complete proof, omitting definitions and complete subproofs that appear in the high-level view. If a formula  $F$  is a conjunction, we may number the conjuncts and let  $F.i$  denote the  $i^{\text{th}}$  one.

The proof uses the following two lemmas.

**Lemma 3 (Rule WF1)** *For any predicates  $P$ ,  $Q$ , and  $I$ ; actions  $\mathcal{N}$  and  $\mathcal{A}$ ; and state function  $f$ ; if*

1.  $P \wedge I \wedge I' \wedge [\mathcal{N}]_f \Rightarrow (P' \vee Q')$
2.  $P \wedge I \wedge I' \wedge \langle \mathcal{N} \wedge \mathcal{A} \rangle_f \Rightarrow Q'$
3.  $P \wedge I \Rightarrow \text{Enabled } \langle \mathcal{A} \rangle_f.$

then  $\text{WF}_f(\mathcal{A}) \wedge \Box I \wedge \Box[\mathcal{N}]_f \Rightarrow (P \rightsquigarrow Q)$

PROOF: This is a simple generalization of rule WF1 of [13]; the proof of soundness is omitted.

**Lemma 4** For any actions  $\mathcal{A}$  and  $\mathcal{B}$ , state function  $f$ , variable  $x$ , predicate  $P$ , and property  $\Pi$ :

1. (a)  $(\text{Enabled } (x' = f)) = \text{true}.$   
 (b) If  $\mathcal{A}$  and  $\mathcal{B}$  have no primed variables in common, then  
 $(\text{Enabled } \mathcal{A} \wedge \mathcal{B}) = (\text{Enabled } \mathcal{A}) \wedge (\text{Enabled } \mathcal{B}).$
2.  $(\text{Enabled } \mathcal{A}) \wedge (\neg \text{Enabled } \mathcal{B}) \Rightarrow (\text{Enabled } \mathcal{A} \wedge \neg \mathcal{B})$
3.  $(\text{Enabled } P \wedge \mathcal{A}) = (P \wedge \text{Enabled } \mathcal{A})$
4.  $(\text{Enabled } \exists x : \mathcal{A}) = (\exists x : \text{Enabled } \mathcal{A})$
5. If  $\mathcal{A} \Rightarrow \mathcal{B}$  then  $(\text{Enabled } \mathcal{A}) \Rightarrow (\text{Enabled } \mathcal{B}).$

PROOF: These properties all follow easily from the definitions.

### B.7.1 High-Level Proof of the Theorem

LET:  $\Pi \stackrel{\Delta}{=} E \wedge M$

$M' \stackrel{\Delta}{=} M \wedge RT_v \wedge$

$\forall i \in I : \text{MinTime}(t_i, \mathcal{A}_i, v) \wedge \forall j \in J : \text{MaxTime}(T_j)$

$\Pi' \stackrel{\Delta}{=} E \wedge M'$

ASSUME: 0. a.  $\Pi = \text{Init} \wedge \Box[\mathcal{N}]_w$  for some predicate  $\text{Init}$ , action  $\mathcal{N}$ , and state function  $w$ .

b.  $t_i$  is a timer for  $\Pi$ , for all  $i$  in  $I$ .

c.  $T_j$  is a timer for  $\Pi$ , for all  $j$  in  $J$ .

d.  $J$  is a finite set.

1.  $\langle \mathcal{A}_i \rangle_v$  and  $\langle \mathcal{A}_j \rangle_v$  are disjoint for  $\Pi$ , for all  $i \in I$  and  $j \in J$  with  $i \neq j$ .

now

2. a. does not occur free in  $v$ .

b.  $(\text{now}' = r) \wedge (v' = v)$  is a subaction of  $\Pi$ , for all  $r \in \mathbf{R}$ .

3. For all  $j \in J$ :

a.  $\langle \mathcal{A}_j \rangle_v \wedge (\text{now}' = \text{now})$  is a subaction of  $\Pi$ .

b.  $\Pi \Rightarrow \text{VTimer}(T_j, \mathcal{A}_j, \Delta_j, v)$  or

$\Pi \Rightarrow \text{PTimer}(T_j, \mathcal{A}_j, \Delta_j, v)$ , where  $\Delta_j \in (0, \infty)$ .

- c.  $\Pi' \Rightarrow \Box(\text{Enabled } \langle \mathcal{A}_j \rangle_v = \text{Enabled } (\langle \mathcal{A}_j \rangle_v \wedge (\text{now}' = \text{now})))$
- d.  $(v' = v) \Rightarrow (\text{Enabled } \langle \mathcal{A}_j \rangle_v = (\text{Enabled } \langle \mathcal{A}_j \rangle_v)')$
- 4.  $\Pi' \Rightarrow \Box(t_k \leq T_k)$ , for all  $k \in I \cap J$ .
- 5.  $E$  and  $M$  are safety properties, and  $M$  constrains at most  $\mu$ .
- 6. a.  $\langle \mathcal{A}_k \rangle_v \Rightarrow \mu$ , for all  $k \in I \cup J$ .
- b.  $(\text{now}' \neq \text{now}) \Rightarrow \mu$

PROVE:  $(E \rightarrow M', NZ)$  is  $\mu$ -machine realizable.

PROOF SKETCH: We show that a fairness condition  $\text{WF}_{(\text{now}, v)}(\mathcal{C})$  implies that  $\text{now}$  increases without bound, for a subaction  $\langle \mathcal{C} \rangle_{(\text{now}, v)}$  of  $\Pi'$ , and then apply Propositions 4 and 5. To prove that  $\text{now}$  increases without bound, we prove that  $\text{now} = r$  leads to  $\text{now} \geq (r + \Delta)$  for any number  $r$ , where  $\Delta$  is the minimum of the  $\Delta_j$ . The action  $\mathcal{C}$  advances  $\text{now}$  or, if that is impossible because  $\text{now} = T_j$  for some upper-bound timer  $T_j$ , it performs the action  $\mathcal{A}_j$  (thereby advancing  $T_j$ ). We begin by naming the next-state relations of the  $RT_v$ ,  $MaxTime$ ,  $MinTime$ ,  $VTimer$ , and  $PTimer$  formulas and defining some actions and predicates, including  $\mathcal{C}$ . A  $\mathcal{B}_j$  step is defined to be an  $\mathcal{A}_j$  step that leaves  $\text{now}$  unchanged. An  $\mathcal{A}_j'$  or  $\mathcal{B}_j'$  step is an  $\mathcal{A}_j$  or  $\mathcal{B}_j$  step that satisfies the lower-bound timing constraint, if there is one. The state function  $T$  is the smallest  $T_j$  that restricts the advance of  $\text{now}$ .

$$\begin{aligned}
\text{LET: } RTact_v &\triangleq [(\text{now}' \in (\text{now}, \infty)) \wedge (v' = v)]_{\text{now}} \\
MaxTact(t) &\triangleq [\text{now}' \leq t']_{\text{now}} \\
MinTact(t, \mathcal{A}, v) &\triangleq [\mathcal{A} \Rightarrow (t \leq \text{now})]_v \\
VTact(t, \mathcal{A}, \delta, v) &\triangleq [\wedge t' = \mathbf{if} (\text{Enabled } \langle \mathcal{A} \rangle_v)' \\
&\quad \mathbf{then if } \langle \mathcal{A} \rangle_v \vee \neg \text{Enabled } \langle \mathcal{A} \rangle_v \\
&\quad \quad \mathbf{then } \text{now} + \delta \\
&\quad \quad \mathbf{else } t \\
&\quad \mathbf{else } \infty \\
&\quad \wedge v' \neq v]_{(t, v)} \\
PTact(t, \mathcal{A}, \delta, v) &\triangleq [\wedge t' = \mathbf{if} \text{Enabled } \langle \mathcal{A} \rangle_v \\
&\quad \mathbf{then if } \langle \mathcal{A} \rangle_v \mathbf{ then } \text{now} + \delta \\
&\quad \quad \mathbf{else } t \\
&\quad \mathbf{else } t + (\text{now}' - \text{now}) \\
&\quad \wedge (\text{now}, v)' \neq (\text{now}, v)]_{(t, v, \text{now})} \\
P_j &\triangleq (j \in I) \Rightarrow (t_j \leq \text{now}) \\
\mathcal{A}_j^t &\triangleq \mathcal{A}_j \wedge P_j \\
\mathcal{B}_j &\triangleq \mathcal{A}_j \wedge (\text{now}' = \text{now}) \\
\mathcal{B}_j^t &\triangleq \mathcal{A}_j^t \wedge (\text{now}' = \text{now})
\end{aligned}$$

$$\begin{aligned}
T &\triangleq \min\{T_j : j \in J \wedge \text{Enabled } \langle \mathcal{A}_j \rangle_v\} \\
\Delta &\triangleq \min\{\Delta_j : j \in J\} \\
\text{Now}T &\triangleq (\text{now}' = \min(\text{now} + \Delta, T)) \wedge (v' = v) \\
\mathcal{C} &\triangleq \vee (T \neq \text{now}) \wedge \text{Now}T \\
&\quad \vee (T = \text{now}) \wedge \exists j \in J : (T_j = T) \wedge \mathcal{B}'_j
\end{aligned}$$

(1)1. Choose  $J_P$  and  $J_V$  such that:

1.  $J = J_P \cup J_V$
2.  $J_P \cap J_V = \emptyset$
3.  $\forall j \in J_V : \Pi \Rightarrow \text{VTimer}(T_j, \mathcal{A}_j, \Delta_j, v)$
4.  $\forall j \in J_P : \Pi \Rightarrow \text{PTimer}(T_j, \mathcal{A}_j, \Delta_j, v)$

PROOF:  $J_P$  and  $J_V$  exist by assumption (0).3b.

(1)2.  $\Pi' \Rightarrow \Box \text{Inv}$ , where

$$\begin{aligned}
\text{Inv} &\triangleq 1. \wedge \forall j \in J : T_j \in [\text{now}, \infty] \\
&\quad 2. \wedge \text{now} \in \mathbf{R} \\
&\quad 3. \wedge \forall j \in J : (\text{Enabled } \langle \mathcal{A}_j \rangle_v = \\
&\quad \quad \quad \text{Enabled } (\langle \mathcal{A}_j \rangle_v \wedge (\text{now}' = \text{now}))) \\
&\quad 4. \wedge \forall k \in I \cap J : t_k \leq T_k \\
&\quad 5. \wedge \forall j \in J_V : \neg \text{Enabled } \langle \mathcal{A}_j \rangle_v \Rightarrow (T_j = \infty)
\end{aligned}$$

PROOF SKETCH: It follows immediately from the hypotheses that  $\Pi'$  implies  $\Box \text{Inv}.3$  and  $\Box \text{Inv}.4$ . The proofs for the other conjuncts of  $\text{Inv}$  are straightforward but somewhat tedious invariance proofs, which are omitted.

(2)1.  $\text{RT}_v \Rightarrow \Box \text{Inv}.2$

(2)2. For all  $j \in J : \Pi \wedge \Box \text{Inv}.2 \wedge \text{MaxTime}(T_j) \Rightarrow \Box \text{Inv}.1$

(2)3.  $\Pi' \Rightarrow \Box \text{Inv}.3$

(2)4.  $\Pi' \Rightarrow \Box \text{Inv}.4$

(2)5.  $\Pi' \Rightarrow \Box \text{Inv}.5$

(2)6. Q.E.D.

PROOF: (2)1–(2)5.

(1)3. 1.  $\Pi = \text{Init} \wedge \Box \mathcal{M}$

2.  $\Pi' = \text{Init}' \wedge \Box \mathcal{N}'$ , for some predicate  $\text{Init}'$ .

where

$$\begin{aligned}
\mathcal{M} &\triangleq \wedge [\mathcal{N}]_w \\
&\quad \wedge \forall j \in J_P : \text{PTact}(T_j, \mathcal{A}_j, \Delta_j, v) \\
&\quad \wedge \forall j \in J_V : \text{VTact}(T_j, \mathcal{A}_j, \Delta_j, v)
\end{aligned}$$

$$\begin{aligned}
\mathcal{N}' &\triangleq \wedge \mathcal{M} \\
&\quad \wedge \text{RTact}_v \\
&\quad \wedge \forall i \in I : \text{MinTact}(t_i, \mathcal{A}_i, v) \\
&\quad \wedge \forall j \in J : \text{MaxTact}(T_j)
\end{aligned}$$



PROOF: 1 follows from assumptions (0).0a and (0).3b, and 2 follows from the definition of  $\Pi^t$ , since  $\square$  distributes over  $\wedge$ . (A simple calculation shows that  $\mathcal{M} = [\mathcal{M}]_f$  and  $\mathcal{N}^t = [\mathcal{N}^t]_g$ , for suitable tuples  $f$  and  $g$ , so  $\square\mathcal{M}$  and  $\square\mathcal{N}^t$  are TLA formulas.)

(1)4.  $\langle \mathcal{C} \rangle_{(now,v)}$  is a subaction of  $\Pi^t$ .

(2)1. For all  $j$  in  $J$ :  $\langle \mathcal{B}_j^t \rangle_v$  is a subaction of  $\Pi^t$ .

(2)2.  $\langle NowT \rangle_{now}$  is a subaction of  $\Pi^t$ .

(2)3. Q.E.D.

PROOF: By (2)1 and (2)2, since  $\langle NowT \rangle_{now} = \langle NowT \rangle_{(now,v)}$ ,  $\langle \mathcal{B}_j^t \rangle_v = \langle \mathcal{B}_j^t \rangle_{(now,v)}$ , Lemma 4.4 implies that the disjunction of subactions is a subaction, and Lemma 4.3 implies that if  $\mathcal{D}$  is a subaction, then  $P \wedge \mathcal{D}$  is also a subaction, for any predicate  $P$ .

(1)5.  $\Pi^t \wedge \mathbf{WF}_{(now,v)}(\mathcal{C}) \Rightarrow NZ$

PROOF: By the Lattice Rule [13], it suffices to

ASSUME:  $r \in \mathbf{R}$

PROVE:  $\Pi^t \wedge \mathbf{WF}_{(now,v)}(\mathcal{C}) \Rightarrow ((now = r) \rightsquigarrow (now \in [r + \Delta, \infty)))$

PROOF SKETCH: The standard method of proving that  $now = r$  leads to  $now \in [r + \Delta, \infty)$  is to assume that  $now = r$  and  $now$  is never in  $[r + \Delta, \infty)$ , and derive a contradiction. Step (2)2 below proves that, if  $now$  equals  $r$  and it is never in  $[r + \Delta, \infty)$ , then it is always in  $[r, r + \Delta)$ . It therefore suffices to assume  $now$  is always in  $[r, r + \Delta)$  and derive the contradiction.

The contradiction is obtained by showing that eventually there is no upper-bound timer preventing the advance of  $now$  past  $r + \Delta$ . The timers that could prevent the advance of  $now$  are the ones in the set  $U$  of timers that are less than  $r + \Delta$ . Step (2)4 asserts that  $U$  eventually becomes empty, and (2)5 asserts that time then advances past  $r + \Delta$ .

LET:  $U \triangleq \{j \in J : T_j < r + \Delta\}$

$V \triangleq \{j \in J : now < T_j < r + \Delta\}$

$TimerAct \triangleq \forall j \in J : \vee VTact(T_j, \mathcal{A}_j, \Delta_j, v)$   
 $\vee PTact(T_j, \mathcal{A}_j, \Delta_j, v)$

(2)1.  $Inv \Rightarrow Enabled \langle \mathcal{C} \rangle_{(now,v)}$

(2)2.  $\Pi^t \Rightarrow$

$\square((now = r) \wedge \square(now \in (-\infty, r + \Delta))) \Rightarrow \square(now \in [r, r + \Delta))$

(2)3. ASSUME:  $j \in J$

PROVE: 1.  $\Pi^t \wedge \square(now \in [r, r + \Delta)) \Rightarrow \square((j \notin U) \Rightarrow \square(j \notin U))$

2.  $\Pi^t \wedge \square(now \in [r, r + \Delta)) \Rightarrow \square((j \notin V) \Rightarrow \square(j \notin V))$

(2)4.  $\Pi^t \wedge \square(now \in [r, r + \Delta)) \wedge \mathbf{WF}_{(now,v)}(\mathcal{C}) \Rightarrow \diamond \square(U = \emptyset)$

- ⟨2⟩5.  $\wedge \square(now \in [r, r + \Delta))$   
 $\wedge \square(U = \emptyset)$   
 $\wedge \square Inv$   
 $\wedge \mathbf{WF}_{(now,v)}(\mathcal{C})$   
 $\Rightarrow \mathbf{true} \rightsquigarrow (now \geq r + \Delta)$
- ⟨2⟩6. Q.E.D.  
 PROOF: ⟨2⟩2, ⟨2⟩4, ⟨2⟩5, ⟨1⟩2, and temporal logic.
- ⟨1⟩6.  $(E \rightarrow M', E \Rightarrow \mathbf{WF}_{(now,v)}(\mathcal{C}))$  is  $\mu$ -machine realizable.
- ⟨2⟩1.  $M'$  constrains at most  $\mu$ .
- ⟨2⟩2. Q.E.D.  
 PROOF: We apply Proposition 4, with  $E$  substituted for  $\Phi$ ,  $M'$  substituted for  $\Pi$ , and the single formula  $\mathbf{WF}_{(now,v)}(\mathcal{C})$ . Step ⟨2⟩1 asserts that  $M'$  constrains at most  $\mu$ . The three numbered hypotheses of the proposition are proved as follows:
1. ⟨1⟩4.
  2. Assumptions ⟨0⟩.6a and ⟨0⟩.6b and the definition of  $\mathcal{C}$ .
  3. Vacuous.
- ⟨1⟩7.  $(\mathbf{true}, NZ)$  is  $\mu$ -machine realizable.  
 PROOF: Assumption ⟨0⟩.6b.
- ⟨1⟩8. Q.E.D.  
 PROOF: Proposition 5, using ⟨0⟩.5, ⟨1⟩6, ⟨1⟩7, and ⟨1⟩5.

## B.7.2 Detailed Proof of the Theorem

- ⟨1⟩1. Choose  $J_P$  and  $J_V$  such that:
1.  $J = J_P \cup J_V$
  2.  $J_P \cap J_V = \emptyset$
  3.  $\forall j \in J_V : \Pi \Rightarrow VTimer(T_j, \mathcal{A}_j, \Delta_j, v)$
  4.  $\forall j \in J_P : \Pi \Rightarrow PTimer(T_j, \mathcal{A}_j, \Delta_j, v)$
- ⟨1⟩2.  $\Pi' \Rightarrow \square Inv$ , where
- $$\begin{aligned}
 Inv \triangleq & 1. \wedge \forall j \in J : T_j \in [now, \infty] \\
 & 2. \wedge now \in \mathbf{R} \\
 & 3. \wedge \forall j \in J : (Enabled \langle \mathcal{A}_j \rangle_v = \\
 & \quad Enabled (\langle \mathcal{A}_j \rangle_v \wedge (now' = now)) \\
 & 4. \wedge \forall k \in I \cap J : t_k \leq T_k \\
 & 5. \wedge \forall j \in J_V : \neg Enabled \langle \mathcal{A}_j \rangle_v \Rightarrow (T_j = \infty)
 \end{aligned}$$
- ⟨2⟩1.  $RT_v \Rightarrow \square Inv.2$   
 PROOF: An invariance proof.

- (2)2. For all  $j \in J : \Pi \wedge \Box Inv.2 \wedge MaxTime(T_j) \Rightarrow \Box Inv.1$   
 PROOF: Assumption (0).0c and an invariance proof.
- (2)3.  $\Pi' \Rightarrow \Box Inv.3$   
 PROOF: Assumption (0).3c.
- (2)4.  $\Pi' \Rightarrow \Box Inv.4$   
 PROOF: Assumption (0).4.
- (2)5.  $\Pi' \Rightarrow \Box Inv.5$   
 PROOF: (1)1.3, assumption (0).3d, and an invariance proof.
- (2)6. Q.E.D.
- (1)3. 1.  $\Pi = Init \wedge \Box \mathcal{M}$   
 2.  $\Pi' = Init' \wedge \Box \mathcal{N}'$ , for some predicate  $Init'$ .  
 where  

$$\mathcal{M} \triangleq \wedge [\mathcal{N}]_w$$

$$\wedge \forall j \in J_P : PTact(T_j, \mathcal{A}_j, \Delta_j, v)$$

$$\wedge \forall j \in J_V : VTact(T_j, \mathcal{A}_j, \Delta_j, v)$$

$$\mathcal{N}' \triangleq \wedge \mathcal{M}$$

$$\wedge RTact_v$$

$$\wedge \forall i \in I : MinTact(t_i, \mathcal{A}_i, v)$$

$$\wedge \forall j \in J : MaxTact(T_j)$$
- (1)4.  $\langle \mathcal{C} \rangle_{(now, v)}$  is a subaction of  $\Pi'$ .
- (2)1. For all  $j$  in  $J : \langle \mathcal{B}'_j \rangle_v$  is a subaction of  $\Pi'$ .  
 ASSUME:  $j \in J$   
 PROVE:  $\Pi' \Rightarrow \Box (Enabled \langle \mathcal{B}'_j \rangle_v \Rightarrow Enabled (\langle \mathcal{B}'_j \rangle_v \wedge \mathcal{N}'))$
- (3)1.  $\Pi \Rightarrow \Box (Enabled (\langle \mathcal{B}'_j \rangle_v \wedge \mathcal{M}) \Rightarrow$   
 $Enabled (\wedge \langle \mathcal{B}'_j \rangle_v \wedge \mathcal{M}$   
 $\wedge \forall i \in I - \{j\} : \neg(\langle \mathcal{A}_i \rangle_v \wedge \langle \mathcal{A}_j \rangle_v \wedge \mathcal{M})))$
- (4)1.  $\Pi \Rightarrow \neg Enabled (\exists i \in I - \{j\} : \langle \mathcal{A}_i \rangle_v \wedge \langle \mathcal{A}_j \rangle_v \wedge \mathcal{M})$   
 PROOF: (1)3.1, assumption (0).1, and Lemma 4.4.
- (4)2. Q.E.D.  
 PROOF: Lemma 4.2, substituting  $\langle \mathcal{B}'_j \rangle_v \wedge \mathcal{M}$  for  $\mathcal{A}$  and  $\exists i \in I - \{j\} :$   
 $\langle \mathcal{A}_i \rangle_v \wedge \langle \mathcal{A}_j \rangle_v \wedge \mathcal{M}$  for  $\mathcal{B}$ .
- (3)2.  $\wedge \langle \mathcal{B}'_j \rangle_v \wedge \mathcal{N}'$   
 $\wedge \forall i \in I - \{j\} : \neg(\langle \mathcal{A}_i \rangle_v \wedge \langle \mathcal{A}_j \rangle_v \wedge \mathcal{M})$   
 $= \wedge \langle \mathcal{B}'_j \rangle_v \wedge \mathcal{M}$   
 $\wedge \forall i \in I - \{j\} : \neg(\langle \mathcal{A}_i \rangle_v \wedge \langle \mathcal{A}_j \rangle_v \wedge \mathcal{M})$
- PROOF:  $\wedge \langle \mathcal{B}'_j \rangle_v \wedge \mathcal{N}'$   
 $\wedge \forall i \in I - \{j\} : \neg(\langle \mathcal{A}_i \rangle_v \wedge \langle \mathcal{A}_j \rangle_v \wedge \mathcal{M})$

$$\begin{aligned}
&= [\text{by definition of } \langle \mathcal{B}_j^t \rangle_v] \\
&\quad \wedge \mathcal{A}_j^t \wedge (v' \neq v) \wedge (now' = now) \wedge \mathcal{N}^t \\
&\quad \wedge \forall i \in I - \{j\} : \neg(\langle \mathcal{A}_i \rangle_v \wedge \langle \mathcal{A}_j \rangle_v \wedge \mathcal{M}) \\
&= [\text{by definition of } \mathcal{N}^t] \\
&\quad \wedge \mathcal{A}_j^t \wedge \mathcal{M} \wedge (v' \neq v) \wedge (now' = now) \\
&\quad \wedge \forall i \in I : \mathcal{A}_i \Rightarrow (t_i \leq now) \\
&\quad \wedge \forall i \in I - \{j\} : \neg(\langle \mathcal{A}_i \rangle_v \wedge \langle \mathcal{A}_j \rangle_v \wedge \mathcal{M}) \\
&= [\text{by definition of } \mathcal{A}_j^t] \\
&\quad \wedge P_j \wedge \mathcal{A}_j \wedge \mathcal{M} \wedge (v' \neq v) \wedge now' = now \\
&\quad \wedge \forall i \in I : \mathcal{A}_i \Rightarrow (t_i \leq now) \\
&\quad \wedge \forall i \in I - \{j\} : \neg(\mathcal{A}_i \wedge \mathcal{A}_j \wedge \mathcal{M} \wedge (v' \neq v)) \\
&= [\text{by predicate logic}] \\
&\quad \wedge P_j \wedge \mathcal{A}_j \wedge \mathcal{M} \wedge (v' \neq v) \wedge (now' = now) \\
&\quad \wedge (j \in I) \Rightarrow (t_j \leq now) \\
&\quad \wedge \forall i \in I - \{j\} : \neg(\mathcal{A}_i \wedge \mathcal{A}_j \wedge \mathcal{M} \wedge (v' \neq v)) \\
&= [\text{by definition of } \langle \mathcal{A}_k \rangle_v \text{ and } \mathcal{M}] \\
&\quad \wedge P_j \wedge \mathcal{A}_j \wedge \mathcal{M} \wedge (v' \neq v) \wedge (now' = now) \\
&\quad \wedge (j \in I) \Rightarrow (t_j \leq now) \\
&\quad \wedge \forall i \in I - \{j\} : \neg(\langle \mathcal{A}_i \rangle_v \wedge \langle \mathcal{A}_j \rangle_v \wedge \mathcal{M}) \\
&= [\text{by definition of } P_j] \\
&\quad \wedge P_j \wedge \mathcal{A}_j \wedge (v' \neq v) \wedge (now' = now) \wedge \mathcal{M} \\
&\quad \wedge \forall i \in I - \{j\} : \neg(\langle \mathcal{A}_i \rangle_v \wedge \langle \mathcal{A}_j \rangle_v \wedge \mathcal{M}) \\
&= [\text{by definition of } \langle \mathcal{B}_j^t \rangle_v] \\
&\quad \wedge \langle \mathcal{B}_j^t \rangle_v \wedge \mathcal{M} \\
&\quad \wedge \forall i \in I - \{j\} : \neg(\langle \mathcal{A}_i \rangle_v \wedge \langle \mathcal{A}_j \rangle_v \wedge \mathcal{M}) \\
\langle 3 \rangle 3. &\quad \Pi \Rightarrow \square (Enabled (\langle \mathcal{B}_j^t \rangle_v \wedge \mathcal{M}) \Rightarrow Enabled (\langle \mathcal{B}_j^t \rangle_v \wedge \mathcal{N}^t)) \\
&\quad \text{PROOF: } \langle 3 \rangle 1, \langle 3 \rangle 2, \text{ and Lemma 4.5.} \\
\langle 3 \rangle 4. &\quad \Pi \Rightarrow \square (P_j \wedge Enabled \langle \mathcal{B}_j \rangle_v \Rightarrow P_j \wedge Enabled (\langle \mathcal{B}_j \rangle_v \wedge \mathcal{M})) \\
&\quad \text{PROOF: } \langle 1 \rangle 3, \text{ Assumption } \langle 0 \rangle .3a, \text{ and the definition of } \mathcal{B}_j. \\
\langle 3 \rangle 5. &\quad \Pi \Rightarrow \square ((Enabled \langle \mathcal{B}_j^t \rangle_v) \Rightarrow Enabled (\langle \mathcal{B}_j^t \rangle_v \wedge \mathcal{M})) \\
&\quad \text{PROOF: } \langle 3 \rangle 4, \text{ the definition of } \mathcal{B}_j^t, \text{ and Lemma 4.3.} \\
\langle 3 \rangle 6. &\quad \Pi \Rightarrow \square ((Enabled \langle \mathcal{B}_j^t \rangle_v) \Rightarrow Enabled (\langle \mathcal{B}_j^t \rangle_v \wedge \mathcal{N}^t)) \\
&\quad \text{PROOF: } \langle 3 \rangle 5 \text{ and } \langle 3 \rangle 3. \\
\langle 3 \rangle 7. &\quad \text{Q.E.D.} \\
&\quad \text{PROOF: } \langle 3 \rangle 6 \text{ and the definition of } \Pi^t, \text{ which implies } \Pi^t \Rightarrow \Pi. \\
\langle 2 \rangle 2. &\quad \langle NowT \rangle_{now} \text{ is a subaction of } \Pi^t. \\
\langle 3 \rangle 1. &\quad \Pi^t \Rightarrow \square ((Enabled \langle NowT \rangle_{now}) \Rightarrow \\
&\quad \quad (Enabled (\langle NowT \rangle_{now} \wedge \mathcal{M})))
\end{aligned}$$

$\langle 4 \rangle 1. \Pi' \Rightarrow \Box((Enabled\ NowT) \Rightarrow (Enabled\ (NowT \wedge \mathcal{M})))$

PROOF:  $\langle 1 \rangle 3$ , assumption  $\langle 0 \rangle 2b$ , and the definition of  $NowT$ , since  $\Pi'$  implies  $\Pi$ .

$\langle 4 \rangle 2. Q.E.D.$

PROOF:  $\langle 4 \rangle 1$  and Lemma 4.3, substituting  $now \neq T$  for  $P$  (since  $\langle NowT \rangle_{now}$  equals  $NowT \wedge (now \neq T)$ ).

$\langle 3 \rangle 2. \Pi' \Rightarrow \Box(Enabled\ (\langle NowT \rangle_{now} \wedge \mathcal{M}) \Rightarrow Enabled\ (\langle NowT \rangle_{now} \wedge \mathcal{N}^t))$

$\langle 4 \rangle 1. (Inv \wedge \langle NowT \rangle_{now} \wedge \mathcal{M}) \Rightarrow (\langle NowT \rangle_{now} \wedge \mathcal{N}^t)$

$\langle 5 \rangle 1. Inv \wedge \langle NowT \rangle_{now} \Rightarrow RTact_v$

PROOF:  $\langle 1 \rangle 3$  ( $Inv.1$  and  $Inv.2$ ).

$\langle 5 \rangle 2. \forall i \in I : \langle NowT \rangle_{now} \Rightarrow MinTact(t_i, \mathcal{A}_i, v)$

PROOF:  $MinTact(t_i, \mathcal{A}_i, v) = [\dots]_v$ , and  $\langle NowT \rangle_{now}$  implies  $v' = v$ .

$\langle 5 \rangle 3. \forall j \in J : Inv \wedge \langle NowT \rangle_{now} \wedge \mathcal{M} \Rightarrow MaxTact(T_j)$

ASSUME: 1.  $j \in J$

2.  $Inv \wedge \langle NowT \rangle_{now} \wedge \mathcal{M}$

PROVE:  $MaxTact(T_j)$

$\langle 6 \rangle 1. CASE: Enabled\ \langle \mathcal{A}_j \rangle_v$

$\langle 7 \rangle 1. now' \leq T_j$

PROOF:  $Inv.1$ ,  $Inv.2$ ,  $\langle NowT \rangle_{now}$ , and the definitions of  $NowT$ , since case assumption  $\langle 6 \rangle$  and the definition of  $T$  imply  $T_j \geq T$ .

$\langle 7 \rangle 2. CASE: j \in J_P$

$\langle 8 \rangle 1. CASE: T'_j = T_j$

PROOF:  $\langle 7 \rangle 1$  and the definition of  $MaxTact(T_j)$ .

$\langle 8 \rangle 2. CASE: T'_j = now + \Delta_j$

PROOF:  $Inv.2$ ,  $\langle NowT \rangle_{now}$ , and definition of  $MaxTact(T_j)$ .

$\langle 8 \rangle 3. Q.E.D.$

PROOF:  $\langle 8 \rangle 1$ ,  $\langle 8 \rangle 2$ , and case assumption  $\langle 7 \rangle$ , since case assumption  $\langle 6 \rangle$  and the definition of  $PTact(T_j, \mathcal{A}_j, \Delta_j, v)$  imply that these are the only possibilities.

$\langle 7 \rangle 3. CASE: j \in J_V$

$\langle 8 \rangle 1. T_j = T'_j$

PROOF: Case assumption  $\langle 7 \rangle$  and the definitions of  $\mathcal{M}$  and  $VTact(T_j, \mathcal{A}_j, \Delta_j, v)$ , since  $\langle NowT \rangle_v$  implies  $v = v'$ .

$\langle 8 \rangle 2. Q.E.D.$

PROOF:  $\langle 7 \rangle 1$ ,  $\langle 8 \rangle 1$ , and the definition of  $MaxTact(T_j)$ .

$\langle 7 \rangle 4. Q.E.D.$

PROOF:  $\langle 7 \rangle 2$ ,  $\langle 7 \rangle 3$ ,  $\langle 1 \rangle 1.1$ , and assumption  $\langle 5 \rangle 1$ .

(6)2. CASE:  $\neg Enabled \langle \mathcal{A}_j \rangle_v$   
 (7)1.  $now, now' \in \mathbf{R}$  and  $now' > now$ .  
 PROOF:  $Inv.1, Inv.2, \langle NowT \rangle_{now}$ , and the definition of  $T$ .  
 (7)2. CASE:  $j \in J_P$   
 (8)1.  $T'_j \geq now'$   
 PROOF: (7)1, case assumption (7),  $Inv.1$ , and the definitions of  $\mathcal{M}$  and  $PTact(T_j, \mathcal{A}_j, \Delta_j, v)$ .  
 (8)2. Q.E.D.  
 PROOF: (8)1 and the definition of  $MaxTact(T_j)$ .  
 (7)3. CASE:  $j \in J_V$   
 (8)1.  $T_j = \infty$   
 PROOF: By case assumption (7) and  $Inv.5$ .  
 (8)2.  $T'_j = \infty$   
 PROOF: (8)1, case assumption (7), and the definitions of  $\mathcal{M}$  and  $VTact(T_j, \mathcal{A}_j, \Delta_j, v)$ , since  $\langle NowT \rangle_v$  implies  $v = v'$ .  
 (8)3. Q.E.D.  
 PROOF: (7)1, (8)2, and the definition of  $MaxTact(T_j)$ .  
 (7)4. Q.E.D.  
 PROOF: (7)2, (7)3, (1)1.1, and assumption (5).1.  
 (6)3. Q.E.D.  
 PROOF: (6)1 and (6)2.  
 (5)4. Q.E.D.  
 PROOF: (5)1, (5)2, (5)3, and the definition of  $\mathcal{N}^t$ .  
 (4)2. Q.E.D.  
 PROOF: (4)1 and (1)2, since by Lemma 4.3 and Lemma 4.5,  $Inv \wedge \mathcal{D} \Rightarrow \mathcal{E}$  implies  $\square Inv \Rightarrow \square((Enabled \mathcal{D}) \Rightarrow (Enabled \mathcal{E}))$ , for any actions  $\mathcal{D}$  and  $\mathcal{E}$ .  
 (3)3. Q.E.D.  
 PROOF: (3)1 and (3)2.  
 (2)3. Q.E.D.  
 (1)5.  $\Pi^t \wedge WF_{(now,v)}(\mathcal{C}) \Rightarrow NZ$   
 ASSUME:  $r \in \mathbf{R}$   
 PROVE:  $\Pi^t \wedge WF_{(now,v)}(\mathcal{C}) \Rightarrow ((now = r) \rightsquigarrow (now \in [r + \Delta, \infty)))$   
 LET:  $U \triangleq \{j \in J : T_j < r + \Delta\}$   
 $V \triangleq \{j \in J : now < T_j < r + \Delta\}$   
 $TimerAct \triangleq \forall j \in J : \vee VTact(T_j, \mathcal{A}_j, \Delta_j, v)$   
 $\vee PTact(T_j, \mathcal{A}_j, \Delta_j, v)$

- (2)1.  $Inv \Rightarrow Enabled \langle \mathcal{C} \rangle_{(now,v)}$
- (3)1. CASE:  $T \neq now$   
PROOF: By the definition of  $\mathcal{C}$ , since case assumption (3) implies  $Enabled \langle NowT \rangle_{now}$ .
- (3)2. CASE:  $T = now$
- (4)1. Choose  $j \in J$  such that  $(T_j = T) \wedge Enabled \langle \mathcal{A}_j \rangle_v$ .  
PROOF:  $Inv.2$ , case assumption (3), and the definition of  $T$ .
- (4)2.  $Enabled \langle \mathcal{B}_j \rangle_v$   
PROOF: (4)1,  $Inv.3$ , and the definition of  $\langle \mathcal{B}_j \rangle_v$ .
- (4)3.  $Enabled \langle \mathcal{B}_j^t \rangle_v$   
PROOF: (4)2,  $Inv.4$ , case assumption (3), and the definition of  $\langle \mathcal{B}_j^t \rangle_v$ .
- (4)4. Q.E.D.  
PROOF: Case assumption (3), (4)3, and the definition of  $\mathcal{C}$ .
- (3)3. Q.E.D.  
PROOF: (3)1 and (3)2.
- (2)2.  $\Pi' \Rightarrow$   
 $\Box((now = r) \wedge \Box(now \in (-\infty, r + \Delta)) \Rightarrow \Box(now \in [r, r + \Delta)))$
- (3)1.  $\Box[RTact_v]_{now} \Rightarrow ((now = r) \Rightarrow \Box(now \in [r, \infty)))$   
PROOF: A standard invariance argument.
- (3)2.  $\Box[RTact_v]_{now} \Rightarrow \Box((now = r) \Rightarrow \Box(now \in [r, \infty)))$   
PROOF: (3)1 and simple temporal logic.
- (3)3.  $\Pi' \Rightarrow \Box((now = r) \Rightarrow \Box(r \leq now))$   
PROOF: (3)2, since  $\Pi' \Rightarrow RT_v$  and  $RT_v \Rightarrow \Box[RTact_v]_{now}$ .
- (3)4. Q.E.D.  
PROOF: (3)3, using the temporal logic tautology  
 $(F \rightsquigarrow G) \Rightarrow ((F \wedge \Box H) \rightsquigarrow (G \wedge \Box H))$
- (2)3. ASSUME:  $j \in J$   
PROVE: 1.  $\Pi' \wedge \Box(now \in [r, r + \Delta)) \Rightarrow \Box((j \notin U) \Rightarrow \Box(j \notin U))$   
2.  $\Pi' \wedge \Box(now \in [r, r + \Delta)) \Rightarrow \Box((j \notin V) \Rightarrow \Box(j \notin V))$   
PROOF: A standard invariance proof, using assumption (0).3b.
- (2)4.  $\Pi' \wedge \Box(now \in [r, r + \Delta)) \wedge WF_{(now,v)}(\mathcal{C}) \Rightarrow \Diamond \Box(U = \emptyset)$   
PROOF SKETCH: The set  $V$  consists of those timers in  $U$  that are not equal to  $now$ . To prove that  $U$  is eventually empty, we show that, whenever  $U$  is nonempty, eventually  $U$  or  $V$  gets smaller. Since  $U$  and  $V$  are finite,  $U$  must eventually become empty.
- (3)1. ASSUME:  $U_0$  and  $V_0$  sets, with  $U_0 \neq \emptyset$ .

$$\begin{aligned}
\text{PROVE: } & \wedge \square((U \subseteq U_0) \wedge (V \subseteq V_0)) \\
& \wedge \square(now \in [r, r + \Delta)) \\
& \wedge \square Inv \\
& \wedge \square[TimerAct]_{(now, v)} \\
& \wedge \mathbf{WF}_{(now, v)}(\mathcal{C}) \\
& \Rightarrow ((U = U_0) \wedge (V = V_0)) \rightsquigarrow \\
& \quad (U \subset U_0) \vee ((U \subseteq U_0) \wedge (V \subset V_0))
\end{aligned}$$

PROOF SKETCH: This is a straightforward application of rule WF1 (Lemma 3), with the following substitutions.

$$\begin{aligned}
\text{LET: } I & \triangleq 1. \wedge now \in [r, r + \Delta) \\
& \quad 2. \wedge (U \subseteq U_0) \wedge (V \subseteq V_0) \\
& \quad 3. \wedge Inv \\
P & \triangleq (U = U_0) \wedge (V = V_0) \\
Q & \triangleq (U \subset U_0) \vee ((U \subseteq U_0) \wedge (V \subset V_0)) \\
\mathcal{N} & \triangleq TimerAct \\
\mathcal{A} & \triangleq \mathcal{C} \\
f & \triangleq (now, v)
\end{aligned}$$

$$\langle 4 \rangle 1. I' \Rightarrow (P' \vee Q')$$

PROOF: Obvious.

$$\langle 4 \rangle 2. \text{ ASSUME: } P \wedge I \wedge I' \wedge \langle \mathcal{N} \wedge \mathcal{A} \rangle_f$$

$$\text{PROVE: } Q'$$

$$\langle 5 \rangle 1. \text{ CASE: } T = now$$

$$\langle 6 \rangle 1. \text{ Choose } j \text{ in } J \text{ such that } (T_j = T) \wedge \langle \mathcal{B}'_j \rangle_v.$$

PROOF:  $\mathcal{A}$  and case assumption  $\langle 5 \rangle$ .

$$\langle 6 \rangle 2. T'_j \geq r + \Delta$$

PROOF:  $\langle 6 \rangle 1$ , I.1, the definition of  $\mathcal{B}'_j$ , and *TimerAct*.

$$\langle 6 \rangle 3. j \in U \wedge j \notin U'$$

PROOF:  $\langle 6 \rangle 1$ , case assumption  $\langle 5 \rangle$ , and I.1.

$$\langle 6 \rangle 4. j \notin U'$$

PROOF:  $\langle 6 \rangle 2$ .

$$\langle 6 \rangle 5. \text{ Q.E.D.}$$

PROOF:  $\langle 6 \rangle 3$ ,  $\langle 6 \rangle 4$ , and I'.2.

$$\langle 5 \rangle 2. \text{ CASE: } T \neq now$$

$$\langle 6 \rangle 1. (now' = T) \wedge (v' = v)$$

PROOF:  $\mathcal{A}$  and case assumption  $\langle 5 \rangle$ .

$$\langle 6 \rangle 2. \text{ CASE: } T \in (now, r + \Delta)$$

$$\langle 7 \rangle 1. \text{ Choose } j \text{ in } J \text{ such that } (T_j = T) \wedge Enabled \langle \mathcal{A} \rangle_v.$$

PROOF: Case assumption  $\langle 6 \rangle$  and the definition of  $T$ .



(7)2.  $\vee T'_j = \text{now}'$   
 $\vee T'_j \in [r + \Delta, \infty]$   
 PROOF: (6)1, (7)1, I.1, and *TimerAct*.

(7)3.  $j \in V$   
 PROOF: (7)1, case assumption (6), and the definition of  $V$ .

(7)4.  $j \notin V'$   
 PROOF: (7)2 and the definition of  $V$ .

(7)5. Q.E.D.  
 (7)3, (7)4, and I'.2.

(6)3. CASE:  $T \in [r + \Delta, \infty]$   
 PROOF: Impossible by (6)1 and I'.1.

(6)4. Q.E.D.  
 PROOF: (6)2, (6)3, I.3.1, and case assumption (5).

(5)3. Q.E.D.  
 PROOF: (5)1 and (5)2.

(4)3.  $P \wedge I \Rightarrow \text{Enabled } \langle \mathcal{A} \rangle_f$   
 PROOF: (2)1.

(4)4. Q.E.D.  
 PROOF: (4)1, (4)2, (4)3, and Lemma 3.

(3)2. ASSUME:  $U_0$  and  $V_0$  sets, with  $U_0 \neq \emptyset$ .  
 PROVE:  $\Pi^t \wedge \Box(\text{now} \in [r, r + \Delta)) \wedge \text{WF}_{(\text{now}, v)}(\mathcal{C}) \Rightarrow$   
 $((U = U_0) \wedge (V = V_0)) \rightsquigarrow$   
 $((U \subset U_0) \vee ((U \subseteq U_0) \wedge (V \subset V_0)))$

(4)1.  $\Pi^t \Rightarrow \Box((U \subseteq U_0) \wedge (V \subseteq V_0)) \Rightarrow \Box((U \subseteq U_0) \wedge (V \subseteq V_0))$   
 PROOF: Follows from (2)3.

(4)2. Q.E.D.  
 PROOF: (3)1, (4)1, and (1)2, since  $\Pi^t \Rightarrow \Box[\text{TimerAct}]_{(\text{now}, v)}$  by assumption (0).3b.

(3)3. Q.E.D.  
 PROOF: Since  $U$  and  $V$  are finite by assumption (0).0d, it follows from (3)2 and the Lattice Rule [13] that  $\Pi^t \wedge \Box(\text{now} \in [r, r + \Delta)) \wedge \text{WF}_{(\text{now}, v)}(\mathcal{C})$  implies  $\Diamond(U = \emptyset)$ . By (2)3,  $\Pi^t \wedge \Box(\text{now} \in [r, r + \Delta))$  implies  $\Diamond(U = \emptyset) \Rightarrow \Diamond\Box(U = \emptyset)$ .

(2)5.  $\wedge \Box(\text{now} \in [r, r + \Delta))$   
 $\wedge \Box(U = \emptyset)$   
 $\wedge \Box \text{Inv}$   
 $\wedge \text{WF}_{(\text{now}, v)}(\mathcal{C})$   
 $\Rightarrow \text{true} \rightsquigarrow (\text{now} \geq r + \Delta)$

LET:  $I \triangleq$  1.  $\wedge U = \emptyset$   
           2.  $\wedge now \in [r, r + \Delta)$   
           3.  $\wedge Inv$

$P \triangleq$  true  
 $Q \triangleq now \geq r + \Delta$   
 $\mathcal{N} \triangleq$  true  
 $\mathcal{A} \triangleq \mathcal{C}$   
 $f \triangleq (now, v)$

(3)1.  $I \wedge I' \wedge [\mathcal{N}]_f \Rightarrow (P' \vee Q')$   
       PROOF: Immediate, since  $P' = \text{true}$ .

(3)2. ASSUME:  $P \wedge I \wedge I' \wedge (\mathcal{N} \wedge \mathcal{A})_f$   
       PROVE:  $Q'$

(4)1.  $T \in [r + \Delta, \infty]$   
       PROOF: By  $I$ .

(4)2.  $\langle NowT \rangle_{now}$   
       PROOF: By (4)1,  $I.2$ , and  $\mathcal{A}$ .

(4)3. Q.E.D.  
       PROOF: (4)1, (4)2, and the definition of  $NowT$ .

(3)3.  $P \wedge I \Rightarrow Enabled \langle \mathcal{A} \rangle_f$   
       PROOF: (3)1.

(3)4. Q.E.D.  
       PROOF: (3)1, (3)2, (3)3, and Lemma 3.

(2)6. Q.E.D.

(1)6.  $(E \rightarrow M^t, E \Rightarrow WF_{(now,v)}(\mathcal{C}))$  is  $\mu$ -machine realizable.

(2)1.  $M^t$  constrains at most  $\mu$ .

(3)1.  $M$  constrains at most  $\mu$ .  
       PROOF: Assumption (0).5.

(3)2.  $RT_v$  constrains at most  $\mu$ .  
       PROOF: Assumption (0).6b.

(3)3. For all  $i$  in  $I$ ,  $MinTime(t_i, \mathcal{A}_i, v)$  constrains at most  $\mu$ .  
       PROOF: By definition of  $MinTime$ , a step violates  $MinTime(t_i, \mathcal{A}_i, v)$  only if it is an  $\langle \mathcal{A}_i \rangle_v$  step, so this follows from Assumption (0).6a.

(3)4. For all  $j$  in  $J$ ,  $MaxTime(T_j)$  constrains at most  $\mu$ .  
       PROOF: Assumption (0).6b.

(3)5. Q.E.D.  
       PROOF: (3)1–(3)4 and the definition of  $M^t$ .

(2)2. Q.E.D.

(1)7. (true,  $NZ$ ) is  $\mu$ -machine realizable.  
(1)8. Q.E.D.

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