Settings for the T128 Card (see Figure 15)

Note: see the next section for T128F settings. The default position for all switches on the T128 is Off.

Note: memory-address conflicts with 16-bit VGA video cards are possible if a T128 card is installed, due to the design of this type of VGA card. See Section 3.1 for details, if necessary.

Card Address:

The T128 will work at one of four memory-mapped addresses. Switches 7 and 8 select this address.

Memory-mapped address	Sw7	Sw8
CC00h	OFF	OFF
C800h	OFF	ON
DC00h	ON	OFF
D800h	ON	ON

Note: The T128 uses a single 8K address register beginning at the address selected by the switches.

Full Handshake Interlock:

Switch 4 allows the T128 to wait for a complete handshake from the attached SCSI device(s). This switch is implemented in version 4.25a or later revisions of the Trantor SCSI software. If a SCSI device is having trouble with disk reads or writes, setting this switch to the On position may eliminate the trouble. The Ricoh 5030E2 erasable optical drive, for example, requires this switch to be in the On position, unless switch SW1-1 on the Ricoh drive is in the On position.

Full Handshake Interlock	Sw4
Enabled	ON
Disabled	OFF

at all times.

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Boot ROM enable:

If you wish to boot from the primary SCSI device, and a boot ROM is installed, Switch 5 enables this ROM to permit booting from a device set to SCSI address 0. This switch may be set Off to disable booting, even if the ROM is left in place.

Boot ROM enable	Sw5
Enabled	ON
Disabled	OFF

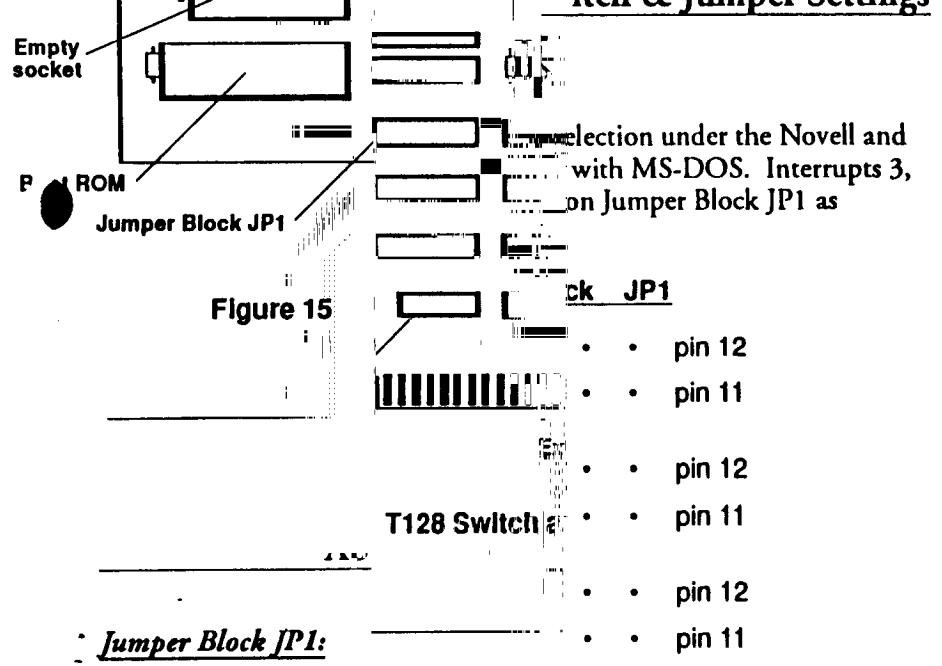
Zero Wait-state Operation:

The T128 may be configured to operate with zero wait-states if Switch 6 is set On. Using zero wait-states permits significantly faster operation of SCSI hard disks or other fast drives, but should only be enabled for AT-class or faster computers. For proper operation in an XT-class computer, leave this switch Off. "Zero Wait-state" does **not** refer to the zero wait-state memory in your computer; rather, it refers to the speed of the bus itself. **Do not** put this switch into the ON position until after you have your installation running correctly, and be sure to test it thoroughly before permanently setting the zero wait state switch in the On position.

Zero Wait-state Operation	Sw6
Enabled	ON
Disabled	OFF

Reserved Switches:

Switches 1, 2 and 3 are reserved and should remain in the Off position



lection under the Novell and with MS-DOS. Interrupts 3, on Jumper Block JP1 as

Figure 15

ck JP1

pin 12
pin 11
pin 12
pin 11
pin 12
pin 11

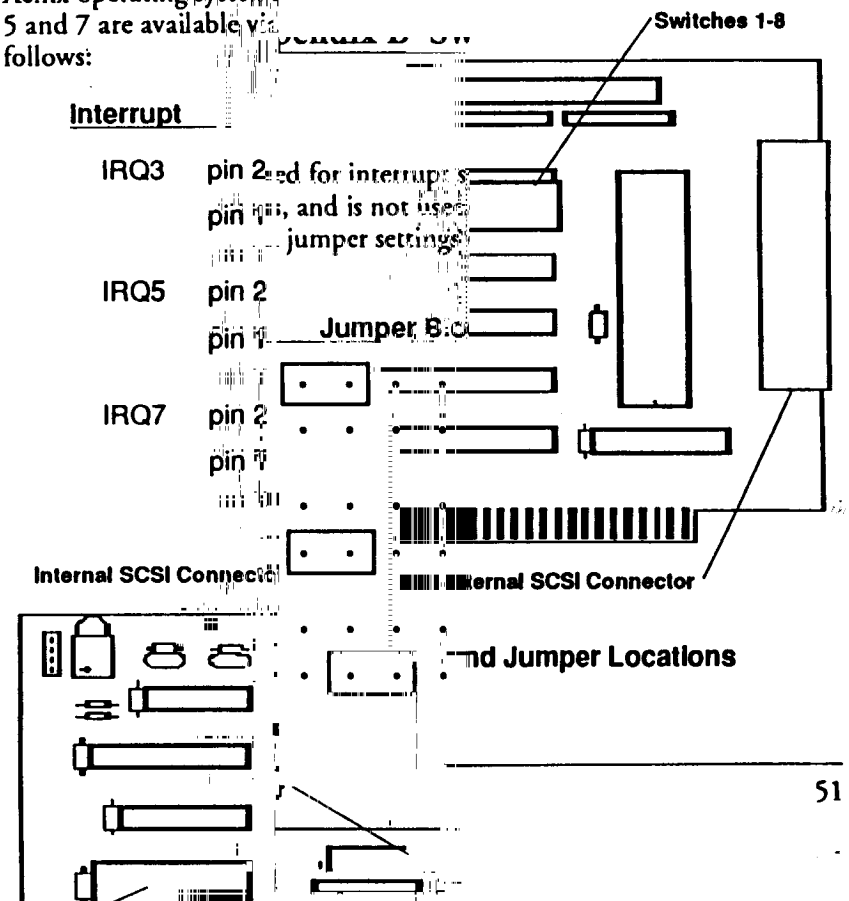
T128 Switch

Jumper Block JP1:

This jumper block is us
Xenix operating system
5 and 7 are available vi
follows:

Interrupt	
IRQ3	pin 2
	pin
IRQ5	pin 2
	pin
IRQ7	pin 2
	pin

ed for interrupt s
, and is not use
jumper settings



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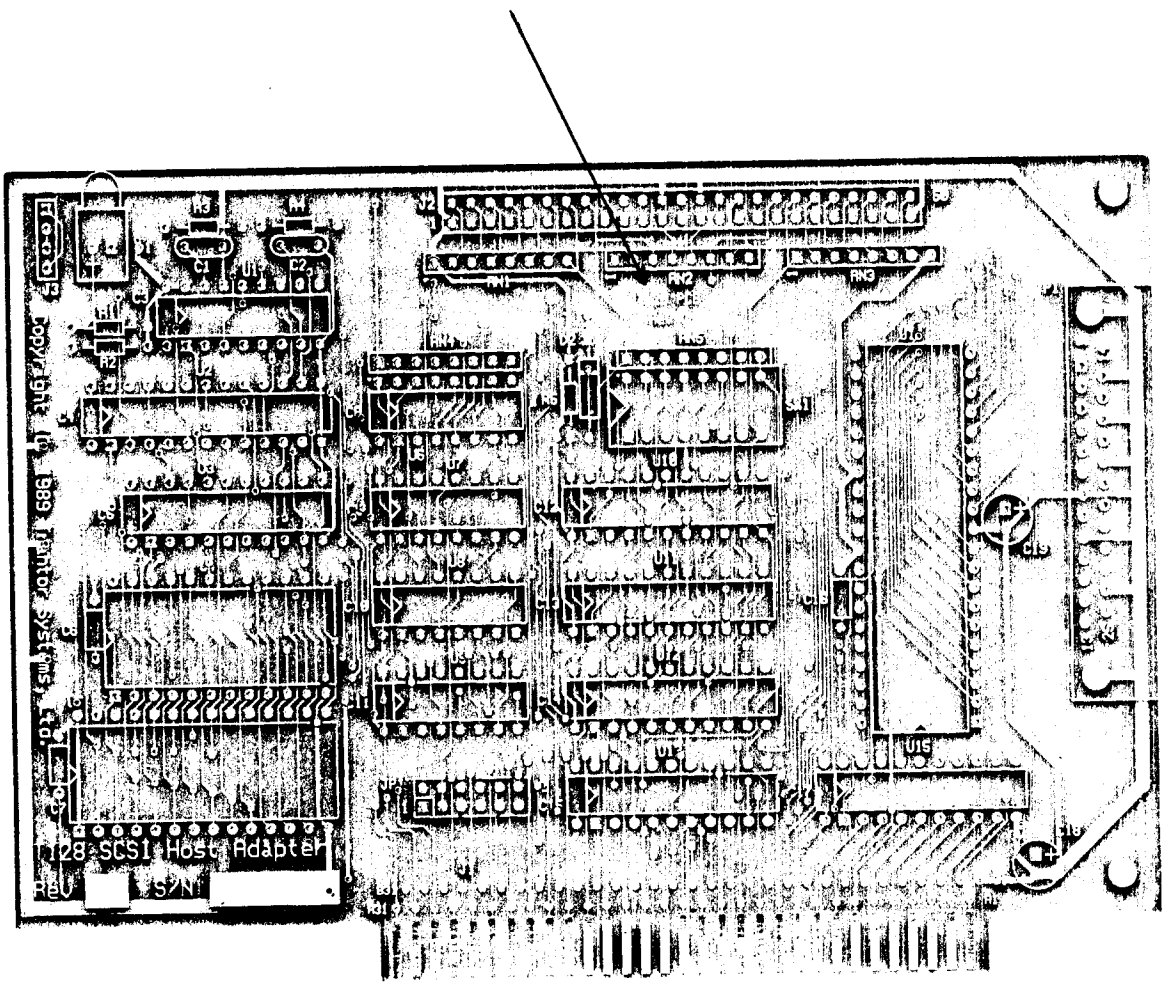
PCB Rev Identification

(no marking for Rev A)



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