

[Deskpro 4000](#)

# Memory

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## Desktop Memory Matrix

	Type	Connector			Error	Std	Max	
Models	RAM	Edge	Pins	Speed	Parity	MBs	MBs	Upgrading
<b>6333 PII</b> 3200 CDS	SDRAM	Gold	168	66 MHz*	Non-Parity /ECC**	32	384	<a href="#">Upgrade Chart</a>
<b>6300 PII</b> 3200 CDS	SDRAM	Gold	168	66 MHz*	Non-Parity /ECC**	32	384	<a href="#">Upgrade Chart</a>
<b>6266 PII</b> 3200 CDS/ML	SDRAM	Gold	168	66 MHz*	Non-Parity /ECC**	32	384	<a href="#">Upgrade Chart</a>
<b>6266 PII</b> 3200 CDS	SDRAM	Gold	168	66 MHz*	Non-Parity /ECC**	32	384	<a href="#">Upgrade Chart</a>
<b>6233 PII</b> 3200 CDS	SDRAM	Gold	168	66 MHz*	Non-Parity /ECC**	32	384	<a href="#">Upgrade Chart</a>
<b>6233 PII</b> 3200 LS	SDRAM	Gold	168	66 MHz*	Non-Parity /ECC**	32	384	<a href="#">Upgrade Chart</a>
<b>6233 PII</b> 2400	SDRAM	Gold	168	66 MHz*	Non-Parity /ECC**	32	384	<a href="#">Upgrade Chart</a>
<b>5233 MMX</b> 3200/CDS	SDRAM	Gold	168	66 MHz*	Non-Parity /ECC**	32	384	<a href="#">Upgrade Chart</a>
<b>5200 MMX</b> 3200/LS	SDRAM	Gold	168	66 MHz*	Non-Parity /ECC**	32	384	<a href="#">Upgrade Chart</a>
<b>5200 MMX</b> 3200/CDS	SDRAM	Gold	168	66 MHz*	Non-Parity /ECC**	32	384	<a href="#">Upgrade Chart</a>
<b>5200 MMX</b> 2400/CDS	SDRAM	Gold	168	66 MHz*	Non-Parity /ECC**	32	384	<a href="#">Upgrade Chart</a>

<b>5200 MMX</b> 2400	SDRAM	Gold	168	66 MHz*	Non-Parity /ECC**	32	384	<a href="#">Upgrade Chart</a>
<b>5166 MMX</b> 3200/LS	SDRAM	Gold	168	66 MHz*	Non-Parity /ECC**	32	384	<a href="#">Upgrade Chart</a>
<b>5166 MMX</b> 2400/CDS	SDRAM	Gold	168	66 MHz*	Non-Parity /ECC**	32	384	<a href="#">Upgrade Chart</a>
<b>5166 MMX</b> 2400	SDRAM	Gold	168	66 MHz*	Non-Parity /ECC**	16	384	<a href="#">Upgrade Chart</a>

\* 66 MHz or faster; SPD JEDEC Rev 1.0 Supported; Unbuffered; CAS Latency 2; Access Time 9ns or faster

<b>All other Models</b>	<b>Type</b>	<b>Connector</b>				<b>Std</b>	<b>Max</b>	
<b>Models</b>	<b>RAM</b>	<b>Edge</b>	<b>Pins</b>	<b>Speed</b>	<b>Parity</b>	<b>MBs</b>	<b>MBs</b>	<b>Upgrading</b>
<b>6233</b> 2500/CDS	EDO	Tin	72	60ns	No	32	192	<a href="#">Upgrade Chart</a>
<b>6200</b> 2500/LS/CDS	EDO	Tin	72	60ns	No	32	256	<a href="#">Upgrade Chart</a>
<b>6180</b> 2500/LS/CDS	EDO	Tin	72	60ns	No	32	256	<a href="#">Upgrade Chart</a>
<b>6180</b> 1620/CDS	EDO	Tin	72	60ns	No	32	256	<a href="#">Upgrade Chart</a>
<b>5200</b> 2500/LS	EDO	Tin	72	60ns	No	32	256	<a href="#">Upgrade Chart</a>
<b>5166</b> 1620, 2500/LS	EDO	Tin	72	60ns	No	16	256	<a href="#">Upgrade Chart</a>
<b>5133</b> 1080, 1620	EDO	Tin	72	60ns	No	16	256	<a href="#">Upgrade Chart</a>
<b>5120</b> 1080,1620/LS	EDO	Tin	72	60ns	No	16	256	<a href="#">Upgrade Chart</a>

	<b>Standard Memory</b>			<b>Slots</b>		
<b>Standard/Max</b>	<b>Soldered</b>	<b>Socketed</b>		<b>Used</b>	<b>Empty</b>	<b>Total</b>
32MB/384	None	One 32MB DIMM		1	2	3
32MB/256	None	Two 16MB SIMMs		2	6	8

32MB/192	None	Two 16MB SIMMs	2	4	6
16MB/256	None	Two 8MB SIMMs	2	6	8

\* 66 MHz or faster; ECC supported; SPD Supported; Unbuffered

\*\*ECC memory is supported but not installed by default. To take advantage of ECC memory the standard Non-parity memory must be removed and only ECC memory may be installed. For ECC memory fault prediction and prefailure warranty to be applicable standard DIMMs must be replaced with ECC DIMMs.

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## Minitower Memory Matrix

	Type	Connector			Error	Std	Max	
Models	RAM	Edge	Pins	Speed	Checking	MBs	MBs	Upgrading
<b>6266 PII</b> 3200 CDS	SDRAM	Gold	168	66 MHz*	Non-Parity /ECC**	32	384	<a href="#">Upgrade Chart</a>
<b>6233 PII</b> 3200 CDS	SDRAM	Gold	168	66 MHz*	Non-Parity /ECC**	32	384	<a href="#">Upgrade Chart</a>
<b>5233 MMX</b> 3200/CDS	SDRAM	Gold	168	66 MHz*	Non-Parity /ECC**	32	384	<a href="#">Upgrade Chart</a>
<b>5166</b> 2500/CDS	EDO	Tin	72	60ns	Non-Parity	32	256	<a href="#">Upgrade Chart</a>
<b>5133</b> 1620/LS	EDO	Tin	72	60ns	Non-Parity	32	256	<a href="#">Upgrade Chart</a>

	Standard Memory		Slots		
Standard/Max	Soldered	Socketed	Used	Empty	Total
32MB/384	None	One 32MB DIMM	1	2	3
32MB/256	None	Two 16MB SIMMs	2	6	8

\* 66 MHz or faster; SPD JEDEC Rev 1.0 Supported; Unbuffered; CAS Latency 2; Access Time 9ns or faster

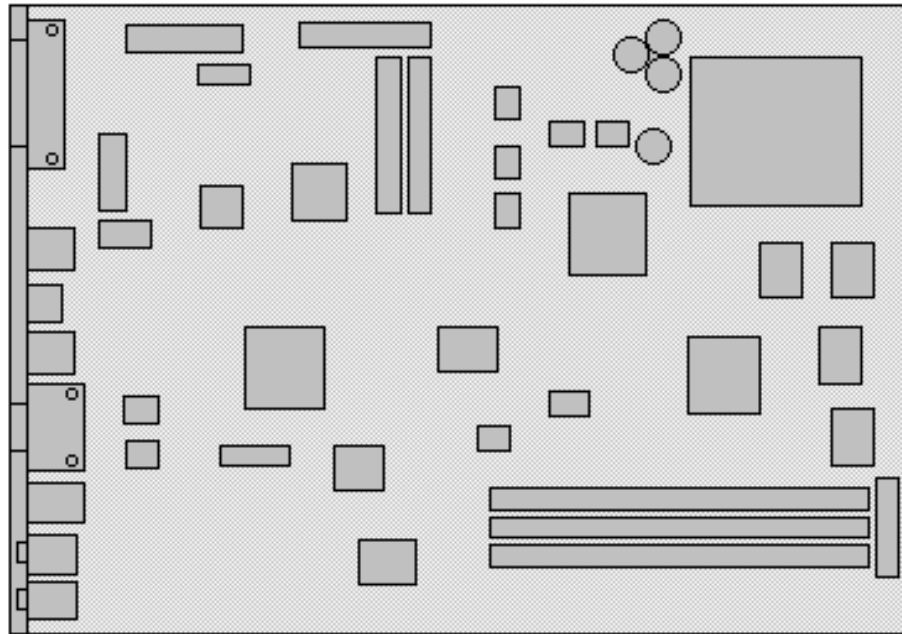
\*\*ECC memory is supported but not installed by default. To take advantage of ECC memory the Non-parity memory must be removed and only ECC memory may be installed. For ECC memory fault prediction and prefailure warranty to be applicable standard DIMMs must be replaced with ECC DIMMs.

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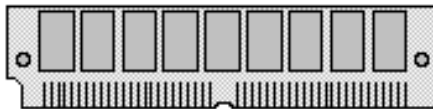
### System Boards 32 MB

P55C: (007294, 007426, 007446, 008067, 008068, 008069)

Pentium II: (007047, 007048, 008096, 008219)



A MAXIMUM OF 256 MB TOTAL MEMORY

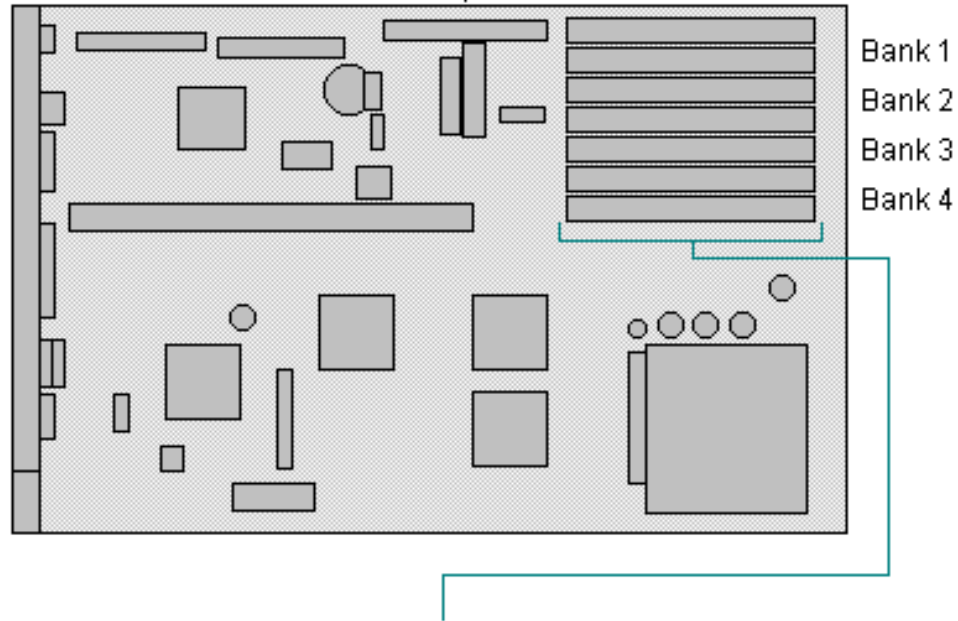


- 8 MB 66 ns MHz DIMMS, EDO - OPTION PN 242802-B21
- 16 MB 66 ns MHz DIMMS, EDO - OPTION PN 242803-B21
- 32 MB 66 ns MHz DIMMS, EDO - OPTION PN 242804-B21
- 64 MB 66 ns MHz DIMMS, EDO - OPTION PN 242805-B21
- 128 MB 66 ns MHz DIMMS, EDO - OPTION PN 242812-B21

### Deskpro 4000 Memory Upgrade Chart

DP4MMX2

PROCESSOR BOARD  
P5: 005580-101, 006178-301  
P6: 005468-101, 006180-301, 007130 (Not Shown)  
PII: 007326, 006650



ADD MEMORY MODULES IN MATCHED PAIRS  
INTO BANKS 2,3 THEN 4 UP TO 256MBs  
TOTAL SYSTEM MEMORY



4-MB 60ns, EDO - OPTION PN 243011-002

8-MB 60ns, EDO - OPTION PN 243012-002

16-MB 60ns, EDO - OPTION PN 243013-002

32-MB 60ns, EDO - OPTION PN 243014-002

**CAUTION:** Your SIMM sockets have tin-lead metal contacts that are silver in appearance. When upgrading your memory, it is important to use SIMMs with tin-lead metal (not gold) to prevent corrosion and/or oxidation resulting from having incompatible metals in contact with each other.

Deskpro 4000 Memory Upgrade Chart

DP4MMX1